

Additional Resources: Product Page PCB Footprint Technical Documentation

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ELECTRICAL CHARACTERISTICS @ 25°C

ELECTRICAL SPECIFICATIONS:

- 1.0

Turns Ratio:

(P6-P5-P4) : (J6-J3)  
(P3-P2-P1) : (J2-J1)

: 1CT : 1CT± 3%  
: 1CT : 1CT ± 3%
- 2.0

Inductance:

(P6-P4)  
(P3-P1)

: 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias  
: 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias
- 3.0

Leakage Inductance:

P6-P4 (WITH J6 AND J3 SHORT)  
P3-P1 (WITH J2 AND J1 SHORT)

: 0.3 MAX. @ 1MHz  
: 0.3 MAX. @ 1MHz
- 4.0

Interwinding Capacitance:

(P6,P5,P4) TO (J6,J3)  
(P3,P2,P1) TO (J2,J1)

: 30pf MAX @ 1MHz  
: 30pf MAX. @ 1MHz
- 5.0

DC Resistance:

(J6-J3)=(J2-J1)

: 1.2 ohms Max.
- 6.0

Return Loss:

1MHz TO 30MHz  
60MHz TO 80MHz

: 18dB MIN.  
: 12dB MIN.
- 7.0

Dielectric Withstand:

(J1, J2) TO (P1, P3)  
(J3, J6) TO (P4,P6)

: 1500 Vrms  
: 1500 Vrms
- 8.0

Insertion Loss:

RS=RL=100 ohms  
100KHz TO 100MHz

: 1.1 dB TYP
- 9.0

Rise Time:

RS=100 OHMS AND RL = 100 OHMS  
Output Voltage = 1 V peak  
Pulse Width= 112ns

: 3.0 nS MAX  
: 3.0 nS MAX
- 10.0

Cross Talk:

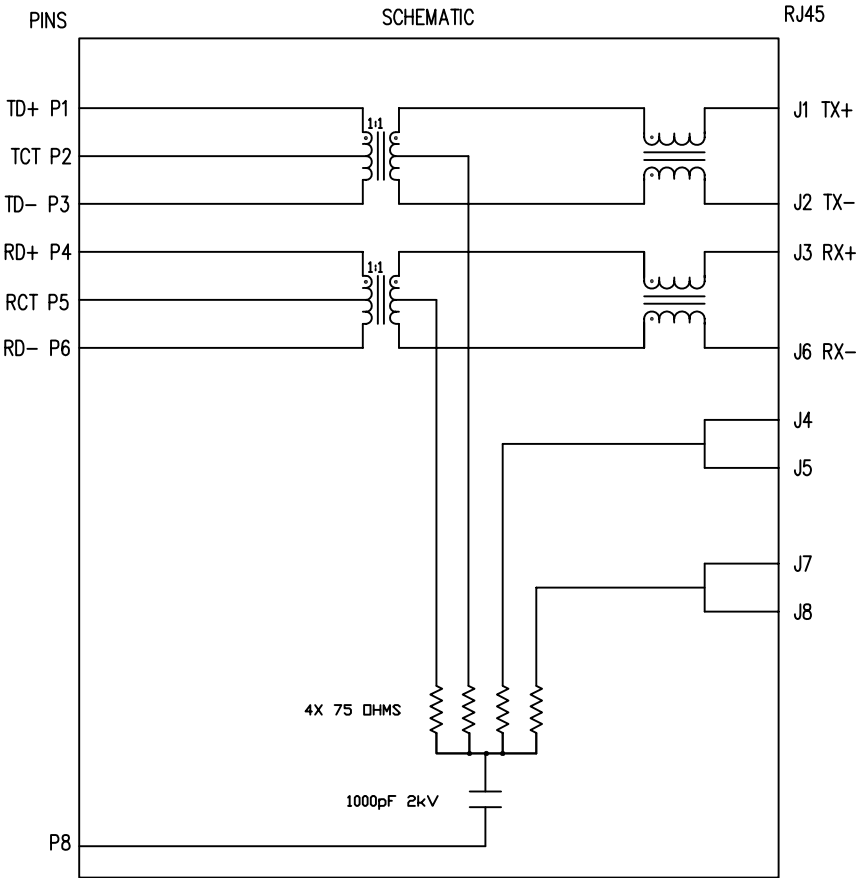
1MHz TO 100MHz

: 40 dB TYP
- 11.0

Common to Common Mode Attenuation:

30MHz TO 100MHz

: 35dB TYP

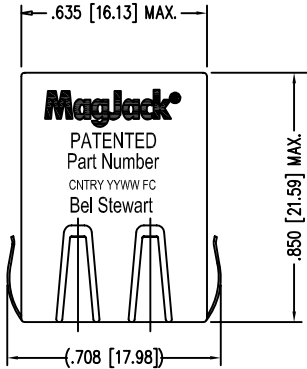


NOTES

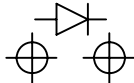
1.0 PINS WITHOUT ELECTRICAL CONNECTION ARE OMITTED.

ORIGINATED BY	DATE	TITLE	PART NO. / DRAWING NO.	STANDARD DIM.	[ ] METRIC DIM. AS REFERENCE		 COMPONENTS FOR A CONNECTED PLANET
Bain Liu	09-04-08	 10/100BT TAB DOWN, SHIELDED PATENTED	SI-60062-F	TOL. IN INCH	UNIT : INCH [mm]	REV. : B0	
DRAWN BY	DATE		FILE NAME	.X	SCALE : N/A	SIZE : A4	
LW Yuan	09-04-08		SI-60062-F.DWG	.XX .XXX		PAGE : 2	

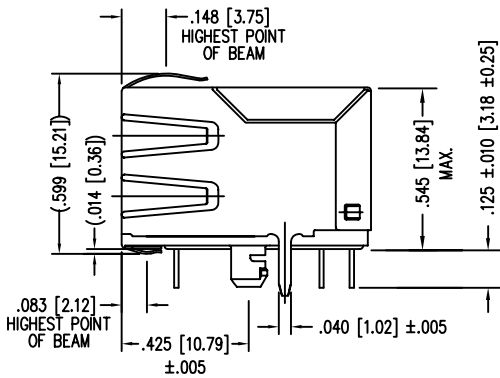
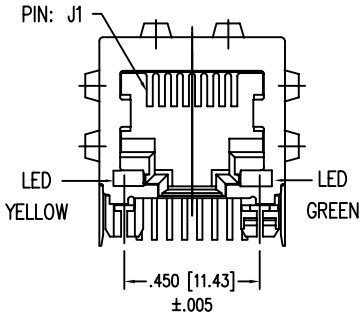
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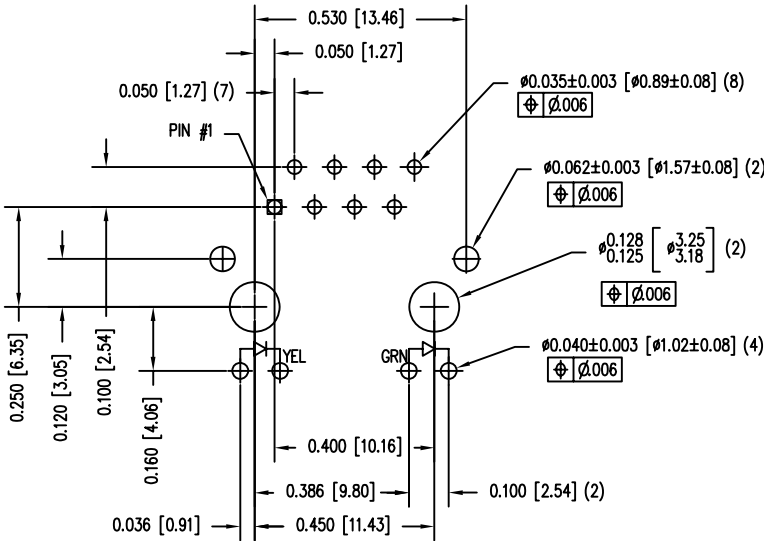
LED POLARITY  
(ENLARGED VIEW)



SINGLE COLOR LED



LED SPECIFICATION			
STANDARD LED	WAVELENGTH	Forward V (MAX)	*(TYP)
GREEN	565 nm	2.5 V	2.2 V
YELLOW	590 nm	2.5 V	2.1 V



P.C.B. RECOMMENDED HOLE LAYOUT  
SEEN FROM COMPONENT SIDE  
ALL CENTERLINE DIMENSIONS ARE BASIC

- NOTES:
1. CONNECTOR MATERIALS:  
HOUSING: THERMOPLASTIC UL94 V-0  
CONTACT/SHIELD: COPPER ALLOY  
SHIELD PLATING: NICKEL OR TIN  
CONTACT PLATING: SELECTIVE GOLD,  
50 MICRO-INCHES MIN. IN CONTACT AREA.
  2. PIN NOT ELECTRICALLY CONNECTED MAYBE OMITTED.  
SEE ELECTRICAL DRAWING FOR OMITTED PINS.
  3. TOLERANCES COMPLY WITH F.C.C. DIMENSION REQUIREMENTS.
  4. ALL TOLERANCES NOT OTHERWISE SPECIFIED TO BE ±.005 [0.13]
  5. WAVE SOLDER COMPATIBLE - PREHEAT 125°C/90SECS.

ORIGINATED BY	DATE
Bain Liu	09-04-08
DRAWN BY	DATE
LW Yuan	09-04-08

TITLE

**MagJack®**  
10/100BT  
TAB DOWN, SHIELDED  
PATENTED

PART NO. / DRAWING NO.
SI-60062-F
FILE NAME
SI-60062-F.DWG

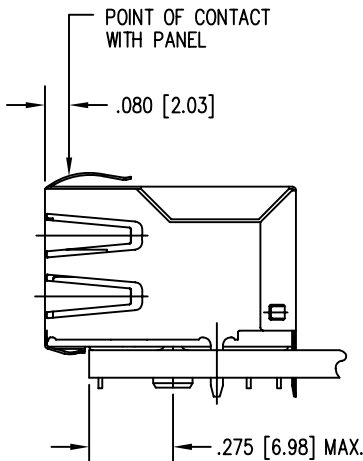
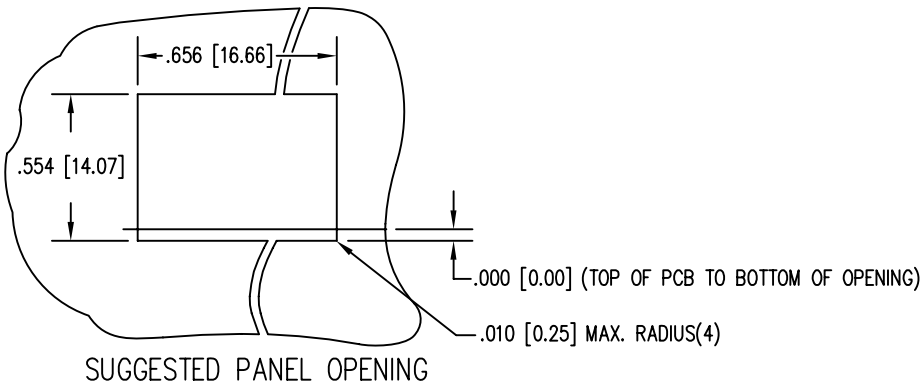
STANDARD DIM.	
TOL. IN INCH	
.X	
.XX	
.XXX	±0.005

[ ] METRIC DIM. AS REFERENCE	
UNIT : INCH [mm]	REV. : B0
SCALE : N/A	SIZE : A4
	PAGE : 3


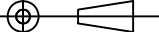


Additional Resources: [Product Page](#) [PCB Footprint](#) [Technical Documentation](#)

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1. THE SUGGESTED PANEL OPENING IS INTENDED TO GIVE THE USER THE ABILITY TO HAVE REASONABLE JACK / PANEL CLEARANCES YET MAINTAIN RELIABLE GROUNDING CAPABILITY.
2. ALL TOLERANCES NOT OTHERWISE SPECIFIED TO BE  $\pm 0.005$  [0.13]

ORIGINATED BY	DATE	TITLE <b>MagJack®</b> 10/100BT TAB DOWN, SHIELDED PATENTED	PART NO. / DRAWING NO.	STANDARD DIM. TOL. IN INCH		[ ] METRIC DIM. AS REFERENCE		 COMPONENTS FOR A CONNECTED PLANET
Bain Liu	09-04-08		SI-60062-F			UNIT : INCH [mm]	REV. : B0	
DRAWN BY	DATE		FILE NAME			SCALE : N/A	SIZE : A4	
LW Yuan	09-04-08		SI-60062-F.DWG	.X			PAGE : 4	
				.XX				
				.XXX	$\pm 0.005$			