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**LED 2 & 4 POLARITY**

<table>
<thead>
<tr>
<th>PIN 11</th>
<th>PIN 12</th>
<th>COLOR</th>
</tr>
</thead>
<tbody>
<tr>
<td>-</td>
<td>+</td>
<td>YELLOW</td>
</tr>
<tr>
<td>+</td>
<td>-</td>
<td>GREEN</td>
</tr>
</tbody>
</table>

**LED 1 & 3 POLARITY**

<table>
<thead>
<tr>
<th>PIN 13</th>
<th>PIN 14</th>
<th>COLOR</th>
</tr>
</thead>
<tbody>
<tr>
<td>-</td>
<td>+</td>
<td>YELLOW</td>
</tr>
<tr>
<td>+</td>
<td>-</td>
<td>GREEN</td>
</tr>
</tbody>
</table>

**ELECTRICAL CHARACTERISTICS @ 25°C**

**TURNS RATIO**

- TP1: 1CT : 1CT ±2%
- TP2: 1CT : 1CT ±2%
- TP3: 1CT : 1CT ±2%
- TP4: 1CT : 1CT ±2%

**OCL @ 100kHz/100mVRMS**

8mA DC BIAS

350μH MIN.

**INS. LOSS**

- 0.1Hz TO 1MHz: -1.1 dB MAX
- 1MHz TO 65MHz: -0.5 dB MAX
- 65MHz TO 100MHz: -0.8 dB MAX
- 100MHz TO 125MHz: -1.2 dB MAX

**RET. LOSS (MIN)**

- 0.5Hz-40MHz: -18 dB
- 40MHz-100MHz: -12+20LOG(f/80MHz) dB

**CM TO CM REJ**

- 100kHz - 100MHz: -30 dB MIN

**CM TO DM REJ**

- 100kHz - 100MHz: -35 dB MIN

**HIPOT (Isolation Voltage):**

1500 Vrms

100% OF PRODUCTION TESTED TO COMPLY WITH IEEE 802.3 ISOLATION REQUIREMENTS.

**LEDS 1, 2, 3 AND 4**

- VF (FORWARD VOLTAGE)
  - IF=20mA: GREEN 2.2V TYP.
  - IF=20mA: YELLOW 2.1V TYP.

- λD (DOMINANT WAVELENGTH)
  - IF=20mA: GREEN 565nm TYP.
  - IF=20mA: YELLOW 590nm TYP.

**Schematic**

- RJ45
- 1 TRP1+
- 2 TRP1-
- 3 TRP2+
- 4 TRP3+
- 5 TRP3-
- 6 TRP2-
- 7 TRP4+
- 8 TRP4-

**LED 2 & 4**

- YELLOW
- GREEN

**LED 1 & 3**

- YELLOW
- GREEN

**PART NO. / DRAWING NO.**

0879-2C4R-54

**STANDARD DIM.**

TOL. IN INCH

**[ ] METRIC DIM. AS REFERENCE**

UNIT: INCH [mm]

**FILE NAME**

0879-2C4R-54_B.DWG

**FILE SIZE**

A4

**SCALE:** N/A

**PAGE:** 2

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PORT ASSIGNMENT

COMPONENT SIDE VIEW

PIN-OUT INFORMATION

COMPONENT SIDE VIEW

<table>
<thead>
<tr>
<th>Pin</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>10</td>
<td>GND</td>
</tr>
<tr>
<td>9</td>
<td>TRDCT1/2/3/4</td>
</tr>
<tr>
<td>8</td>
<td>TRD 4+</td>
</tr>
<tr>
<td>7</td>
<td>TRD 4-</td>
</tr>
<tr>
<td>6</td>
<td>TRD 3+</td>
</tr>
<tr>
<td>5</td>
<td>TRD 3-</td>
</tr>
<tr>
<td>4</td>
<td>TRD 2+</td>
</tr>
<tr>
<td>3</td>
<td>TRD 2-</td>
</tr>
<tr>
<td>2</td>
<td>TRD 1+</td>
</tr>
<tr>
<td>1</td>
<td>TRD 1-</td>
</tr>
<tr>
<td>14</td>
<td>GREEN (-)/YELLOW (+)</td>
</tr>
<tr>
<td>13</td>
<td>GREEN (+)/YELLOW (-)</td>
</tr>
<tr>
<td>12</td>
<td>GREEN (-)/YELLOW (+)</td>
</tr>
<tr>
<td>11</td>
<td>GREEN (-)/YELLOW (-)</td>
</tr>
</tbody>
</table>

THE SHADED AREA ON THE CUSTOMER BOARD ARE RECOMMENDED TO BE CLEAR OFF ANY VIA HOLE OR COMPONENT PAD.

ORIGINATED BY       DATE       TITLE
ANTON LIAO          11-28-08   2X4 gigabit MagJack®
DRAWN BY            DATE       WITH UPPER AND LOWER LED
SANDY LIN           11-28-08   (8 Cores)

PART NO. / DRAWING NO. 0879-2C4R-54

STANDARD DIM. TOL. IN INCH
UNIT : INCH [mm] REV. : B

FILE NAME 0879-2C4R-54_B.DWG
SCALE : N/A SIZE : A4
PAGE : 4

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Recommended PCB front edge cutout. The bottom shield ground tabs (front) should not be seated on the top of the PCB.
SUGGESTED PANEL OPENING

NOTE:
The distance of panel inside surface relative to front surface of part is only a suggestion. In case this distance is different, the required panel opening dimensions change accordingly.

PACKING INFORMATION

PACKING TRAY: 0200-9999-Q1 (TOP)
0200-9999-Q2 (BOTTOM)

PACKING QUANTITY: 20 PCS FINISHED GOODS PER TRAY
7 TRAYS (140 PCS FINISHED GOODS) PER CARTON BOX

NOTE: CARDBOARDS ARE PLACED BETWEEN LAYERS OF PACKING TRAY INSIDE CARTON BOX
(INCLUDE THE UPPERMOST AND LOWERMOST TRAY)