ELECTRICAL CHARACTERISTICS @ 25°C

1.0 TURNS RATIO: (P4–P5–P6) : (J3–J6) = 1CT : 1CT ± 3%
   (P1–P2–P3) : (J1–J2) = 1CT : 1CT ± 3%

2.0 INDUCTANCE: (P4–P6) = 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias
   (P1–P3) = 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias

3.0 LEAKAGE INDUCTANCE: P6–P4 (WITH J6 AND J3 SHORT) = 0.3uH MAX. @ 1MHz
   P1–P3 (WITH J2 AND J1 SHORT) = 0.3uH MAX. @ 1MHz

4.0 INTERWINDING CAPACITANCE: (P6,P5,P4) TO (J6,J3)
   (P3,P2,P1) TO (J2,J1) = 30pF MAX @ 1MHz

5.0 DC RESISTANCE: (J6–J3)=(J2–J1) = 1.35 ohms Max.

6.0 RETURN LOSS: (P4–P6)=100 OHMS AND (P1–P3)=100 OHMS REF.
   1MHz TO 30MHz = −18dB MIN.
   60MHz TO 80MHz = −12dB MIN.

   NOTE: 100 OHMS CONNECTED TO (J2–J1) OR (J6–J3).

7.0 DIELECTRIC WITHSTAND: (J1, J2) TO (P1, P3)
   (J3, J6) TO (P4, P6) = 1500 Vrms

8.0 INSERTION LOSS: RS=RL=100 ohms
   100KHz TO 100MHz = −1.1 dB MAX

9.0 RISE TIME: RS=100 OHMS AND RL = 100 OHMS
   OUTPUT VOLTAGE = 1 V peak
   PULSE WIDTH= 112ns
   = 3.0 nS MAX

10.0 CROSS TALK: 1–100 MHz = −33 dB MIN

11.0 COMMON TO COMMON MODE ATTENUATION: 30MHz TO 100MHz: −35dB MIN

12.0 OPERATING TEMPERATURE RANGE: 0°C TO 70°C

NOTES

1.0 PINS WITHOUT ELECTRICAL CONNECTION ARE OMITTED.
NOTES:

1. CONNECTOR MATERIALS:
   HOUSING: THERMOPLASTIC UL94 V-0
   CONTACT/SHEILD: COPPER ALLOY
   SHIELD PLATING: NICKEL OR TIN
   CONTACT PLATING: SELECTIVE GOLD IN CONTACT AREA.

2. PIN NOT ELECTRICALLY CONNECTED MAY BE OMITTED.
   SEE ELECTRICAL DRAWING FOR OMITTED PINS.

3. TOLERANCES COMPLY WITH F.C.C. DIMENSION REQUIREMENTS.

4. THE PRODUCT IS RoHS COMPLIANT.
   UL RECOGNIZED - FILE #E474585.

5. THE PART IS RECOMMENDED FOR WAVE SOLDERING.
   THE SUGGESTED PEAK WAVE SOLDERING CONDITION IS
   260°C MAX AND 10 SECONDS MAX.
P.C.B. RECOMMENDED HOLE LAYOUT
SEEN FROM COMPONENT SIDE
ALL CENTERLINE DIMENSIONS ARE BASIC

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SUGGESTED PANEL OPENING

THE SUGGESTED PANEL OPENING IS INTENDED TO GIVE THE USER THE ABILITY TO HAVE REASONABLE JACK / PANEL CLEARANCES YET MAINTAIN RELIABLE GROUNDING CAPABILITY.