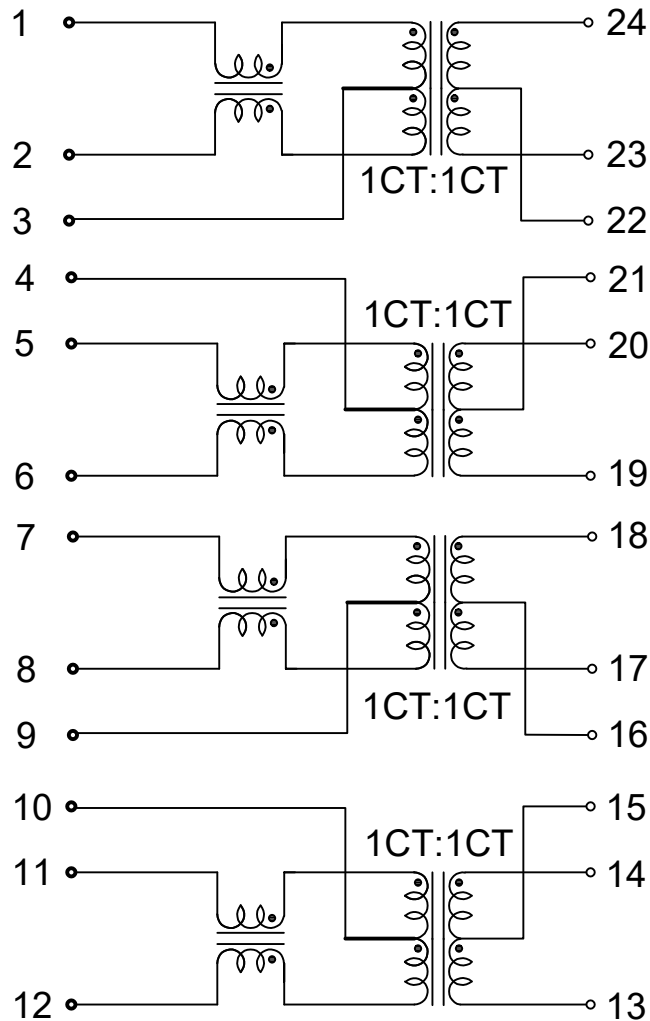


ELECTRICAL SPECEIFICATIONS @25°C

RoHS



URNS RATIO

1CT : 1CT

PHASE

2 TO 23, 5 TO 20,
8 TO 17, 11 TO 14 IN PHASE

CL

(24-23), (20-19), (18-17), (14-13)

350uH MIN. AT 100kHz, 100mV
WITH 0mA DC BIAS,

D.C.R

1.2ΩHM Max

INSERTION LOSS

-1.1dB MAX. @ 1 TO 100MHz

RETURN LOSS

MEASURED UNDER A 100 Ω I/O

-18dB MIN. @ 1-30MHz
-13.1dB MIN. @ 60MHz
-12dB MIN. @ 80MHz
-10dB MIN. @ 100MHz

CROSSTALK

-30dB MIN. AT 10 TO 100 MHz

COMMON TO COMMON REJECTION

-30dB MIN. AT 100kHz TO 100MHz

HIPOT

LEAKAGE CURRENT LESS THAN 1mA AT 1500VAC

ORIGINATED BY		DATE	TITLE	PART NO. / DRAWING NO.	STANDARD DIM.	[] METRIC DIM. AS REF.		REV. : A	PAGE : 1
Roger Shea		2024-9-9	Electrical specification 1000BT PCMCIA module	S558-5999-EF	TOL. IN INCH	UNIT : INCH [mm]			
DRAWN BY		DATE		FILE NAME	.X	SCALE : N/A			
Chen Zhi Jun		2024-9-9		S558-5999-EF_A.DWG	.XX	SIZE : A1			
					.XXX				

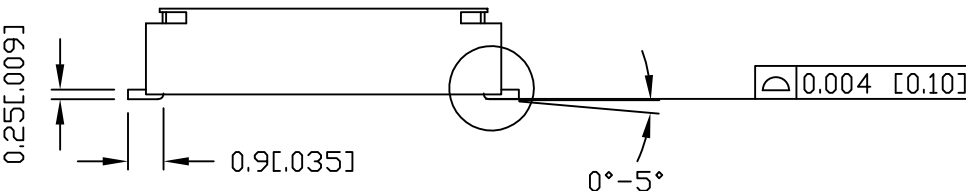
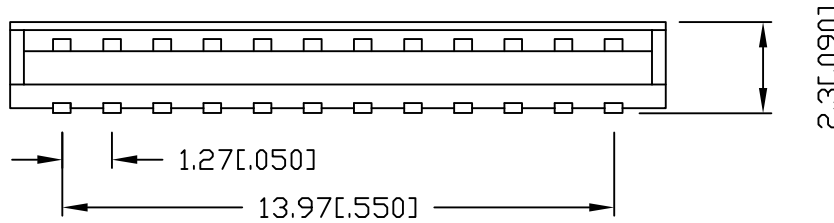
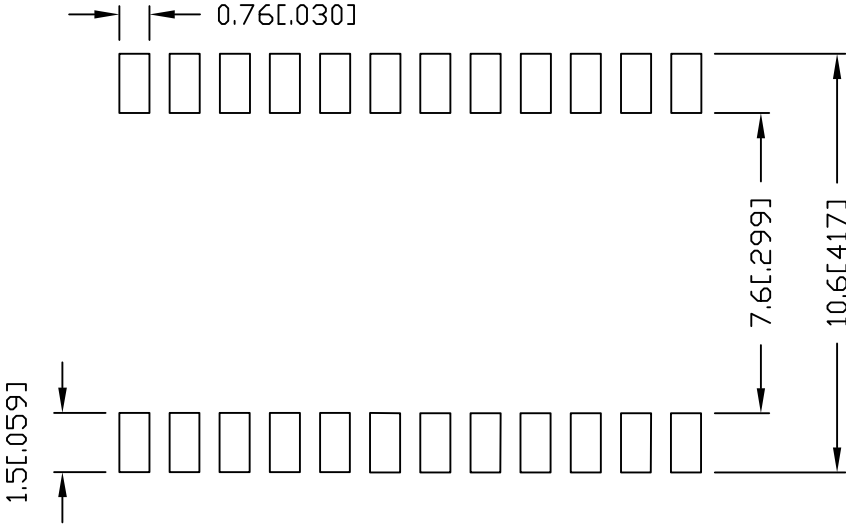
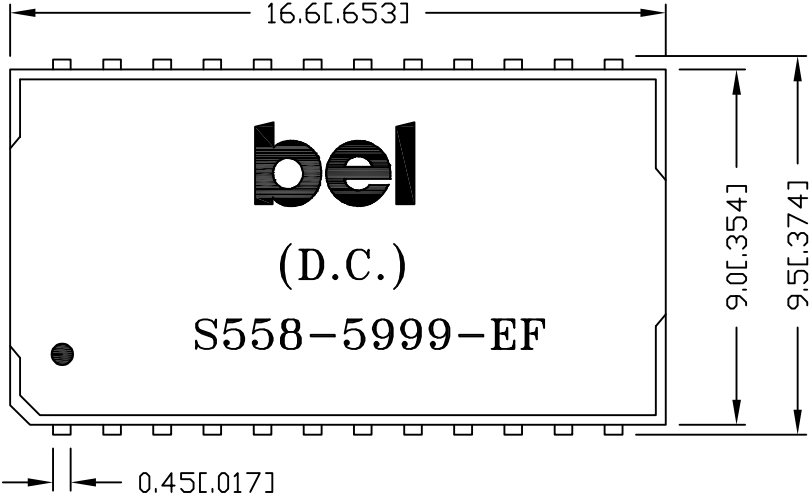


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RoHS



SUGGESTED PCB PAD LAYOUT



NOTES:

- 1. STANDARD MARKING REFER TO DOC. HAND-WORK-04.
- 2. PACKAGE CODE: "RJS001".

2. PACKAGE CODE: "RJS001".							REV. : A		PAGE : 2	
ORIGINATED BY		DATE	TITLE Electrical specification 1000BT PCMCIA module	PART NO. / DRAWING NO.		STANDARD DIM.		[] METRIC DIM. AS REF.		
Roger Shea		2024-9-9		S558-5999-EF		TOL. IN INCH		UNIT : mm[INCH]		
DRAWN BY		DATE		FILE NAME		.X		SCALE : N/A		
Chen Zhi Jun		2024-9-9		S558-5999-EF_A.DWG		.XX ±0.01		SIZE : A1		
						.XXX ±0.005				
										
POWER PROTECT CONNECT										