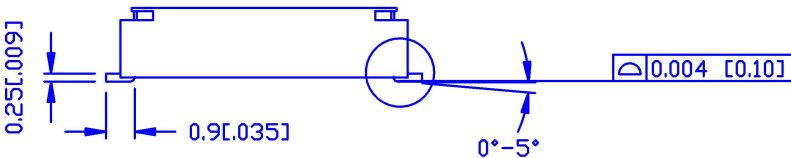
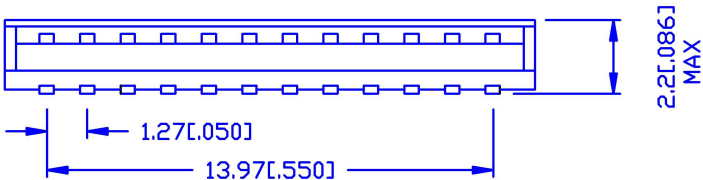
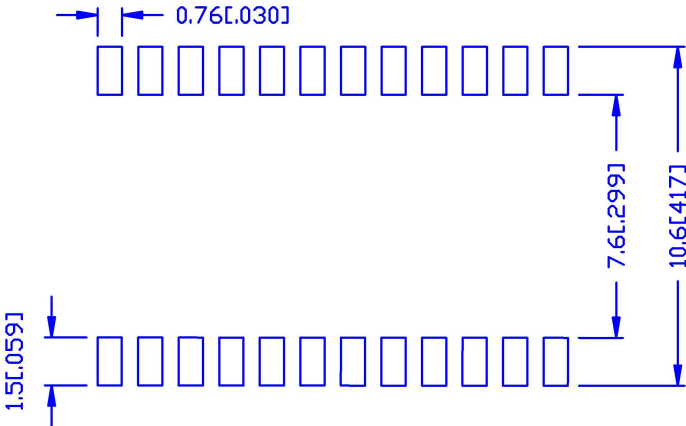
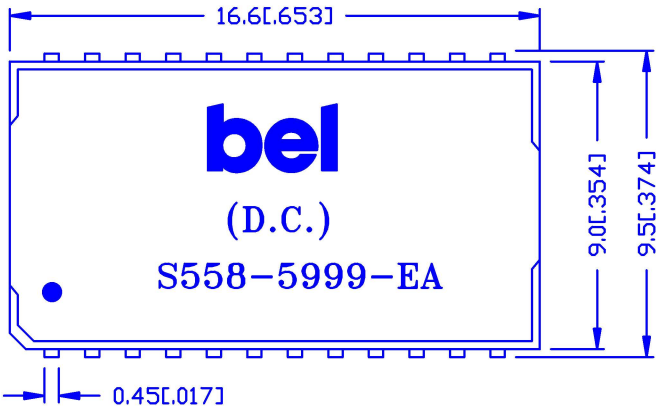


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RoHS



SUGGESTED PCB PAD LAYOUT



NOTES:

1. STANDARD MARKING REFER TO DOC. HAND-WORK-04.
2. PACKAGE CODE: "CQS001".

ORIGINATED BY
SHEA
DATE 2024-10-25
DRAWN BY
CHEN ZHIJUN
DATE 2024-10-25

TITLE
1000Base-T
Transformer module

PART NO. / DRAWING NO.
S558-5999-EA
FILE NAME
S558-5999-EA.DWG

STANDARD DIM. TOL. IN INCH		[] METRIC DIM. AS REF.
.X		UNIT : INCH [mm]
.XX	±0.01	SCALE : N/A
.XXX	±0.005	SIZE : A2

REV. : A PAGE : 1/2

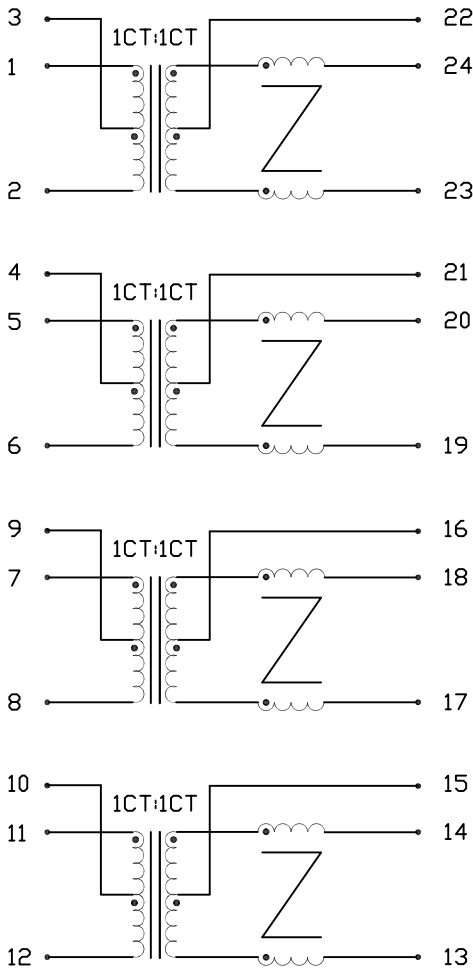


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ELECTRICAL CHARACTERISTICS @ 25°C

SCHEMATIC



Turns Ratio:

PIN(24-22-23) : (1-3-2)
PIN(20-21-19) : (5-4-6)
PIN(18-16-17) : (7-9-8)
PIN(14-15-13) : (11-10-12)

1CT:1CT
1CT:1CT
1CT:1CT
1CT:1CT

Polarity:

PER DOT CONVENTION

DCL (0°C~70°C):

PIN(24-23)=(20-19)=(18-17)=(14-13)

@100kHz, 100mV 350 uH MIN With 8mA DC Bias.

Insertion Loss:

1.20dB MAX. @ 1MHz
1.92dB MAX. @ 125MHz

Return Loss:

16.0dB MIN. @ 0.1 TO 40MHz
14.0dB MIN. @ 50MHz
12.0dB MIN. @ 80MHz
10.0dB MIN. @ 100MHz

CMRR:

30.0dB MIN. @ 1 TO 80MHz

Common to Differential:

42.0dB MIN. @ 2MHz
31.0dB MIN. @ 30MHz
25.0dB MIN. @ 60MHz
21.0dB MIN. @ 100MHz

Crosstalk:

40.0dB MIN. @ 1 TO 40MHz
37.0dB MIN. @ 60MHz
33.0dB MIN. @ 100MHz

HIPOT:

LEAKAGE CURRENT LESS THAN
1mA AT 1500VAC FOR 1 MINUTE

ORIGINATED BY SHEA		TITLE 1000Base-T Transformer module	PART NO. / DRAWING NO. S558-5999-EA		STANDARD DIM. TOL. IN INCH		[] METRIC DIM. AS REF.		 POWER PROTECT CONNECT
DATE 2024-10-25			FILE NAME S558-5999-EA.DWG		.X		UNIT : INCH [mm]		
DRAWN BY CHEN ZHIJUN					.XX		SCALE : N/A		
					.XXX			SIZE : A2	
DATE 2024-10-25									