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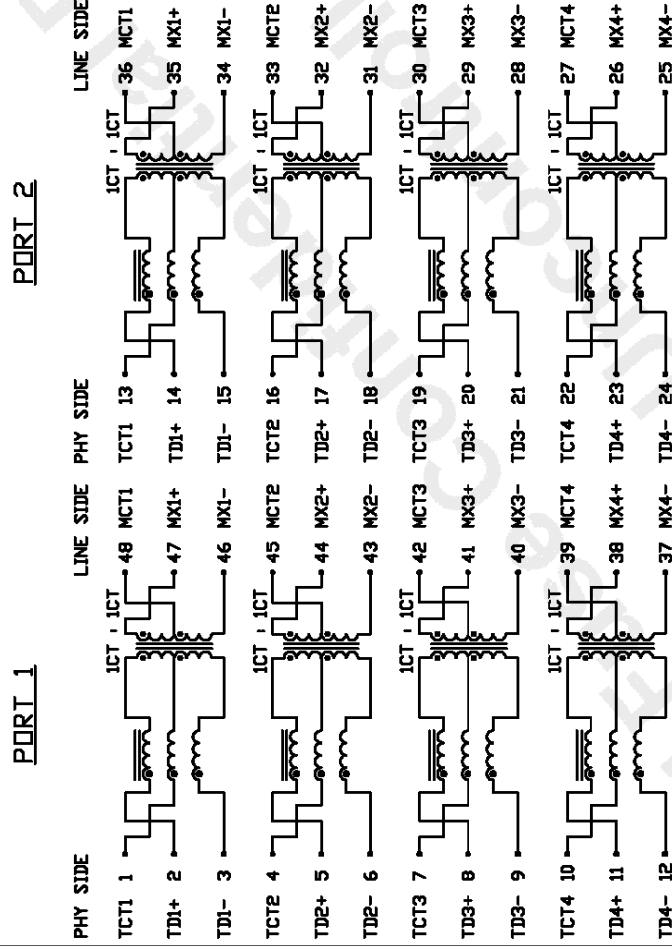
RoHS


ELECTRICAL CHARACTERISTICS @25°C

TURNS RATIO
 PHY : LINE
 POLARITY
 DCL
 DC RESISTANCE (1/2 WINDING)
 INTERWINDING CAPACITANCE, C_w
 INSERTION LOSS
 RETURN LOSS @1000HM
 CROSSTALK
 CM TO CM REJ
 CM TO DM REJ
 BALANCED DC LINE CURRENT
 HIPOT

1CT : 1CT ±2%
 PER DOT CONVENTION
 @100kHz, 100mV
 350uH MIN WITH 11mA DC BIAS
 120uH MIN WITH 19mA DC BIAS
 0.35 OHM MAX
 @1MHz, 20mV
 30pF MAX
 1.1dB MAX
 1.0dB MAX
 1.2dB MAX
 18dB MIN
 12-20LOG(f/80MHz)dB MIN
 33-20LOG(f/100MHz)dB MIN
 30dB MIN
 35dB MIN
 600mA MAX @57VDC CONTINUOUS
 LEAKAGE CURRENT LESS THAN 1mA AT 1500VAC

SCHEMATIC

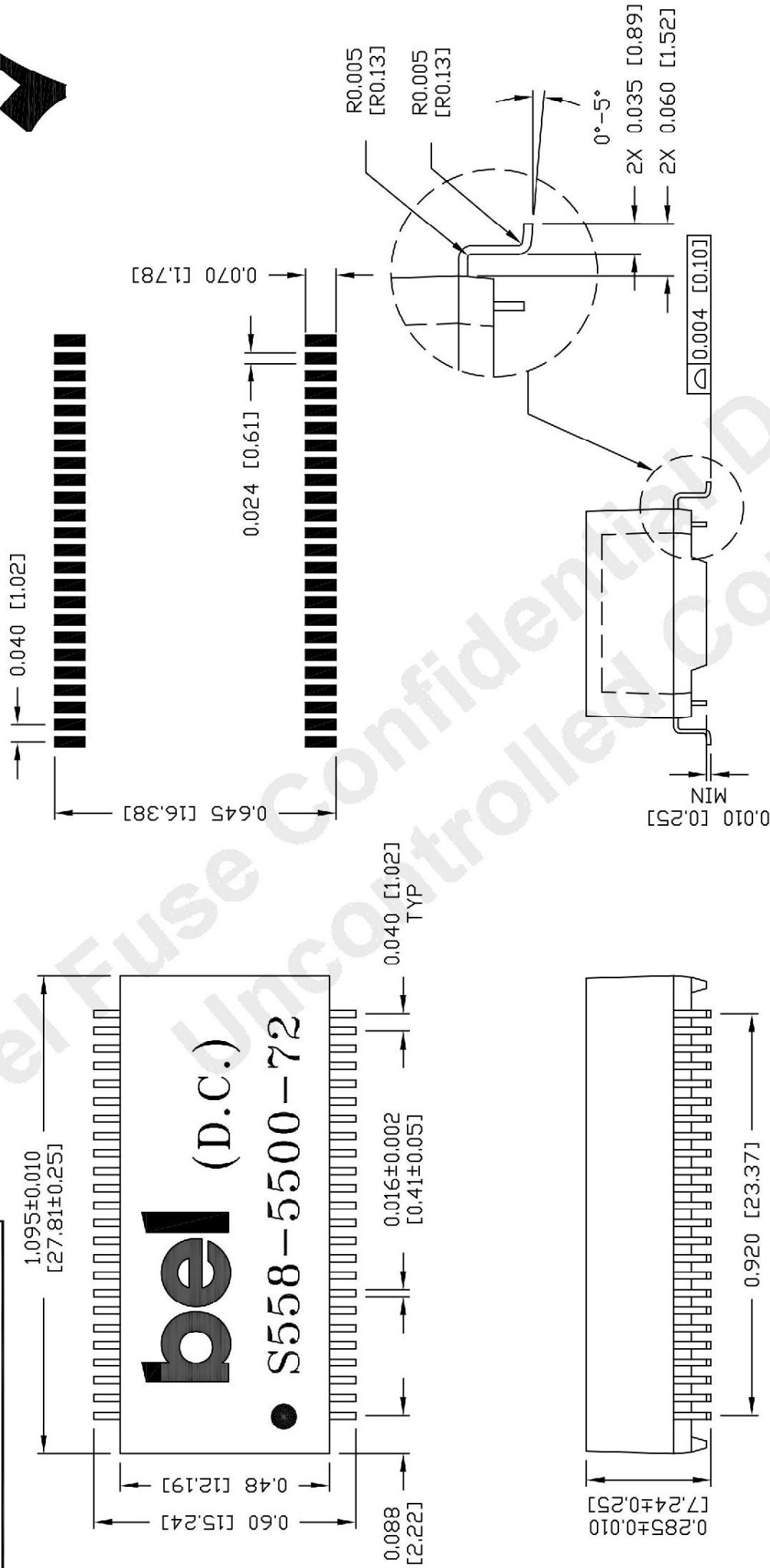


REV. : D	PAGE : 2
 POWER PROTECT CONNECT	
STANDARD DIM. [] METRIC DIM. AS REF.	
TOL. IN INCH	
UNIT : INCH [mm]	
SCALE : N/A	
SIZE : A4	
PART NO. / DRAWING NO.	S558-5500-72
FILE NAME	S558550072.DWG
ELECTRICAL SPECIFICATION	
ORIGINATED BY	DATE
Alice Pang	2020-06-29
DRAWN BY	DATE
ZC Guo	2020-06-29

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RoHS

SUGGESTED PCB PAD LAYOUT



NOTES:

1. STANDARD MARKING REFER TO DOC. HAND-WORK-04.
2. PACKAGE CODE: "QVS001".

ORIGINATED BY: Lawrence Tsang DATE: 2015-10-29		PART NO. / DRAWING NO.: S558-5500-72 FILE NAME: S558550072C.DWG		STANDARD DIM. [] METRIC DIM. AS REF. TOL. IN INCH: UNIT : INCH [mm] .X ±0.01 .XX ±0.005 .XXX ±0.005		REV. : C PAGE : 3
DRAWN BY: ZC Guo DATE: 2015-10-29		TITLE: MECHANICAL OUTLINE		SCALE : N/A SIZE : A4		POWER PROTECT CONNECT

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