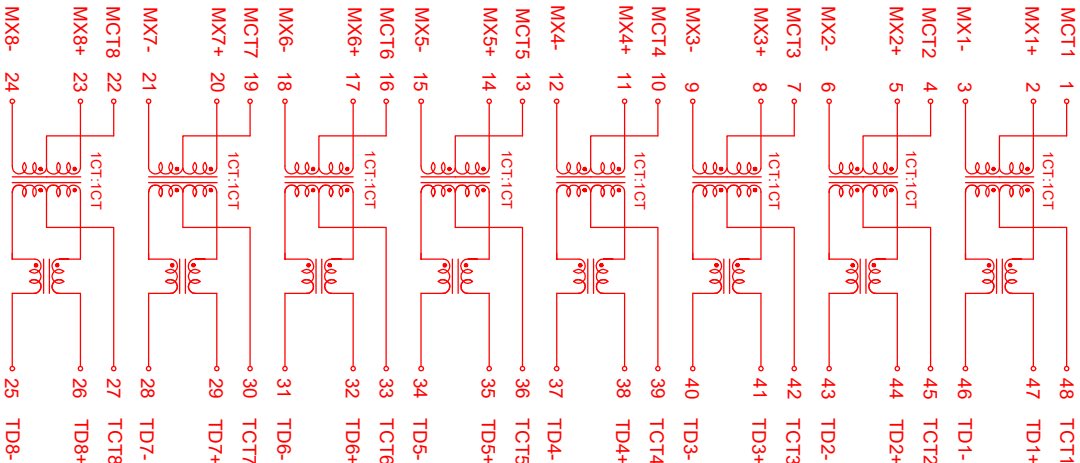


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SCHEMATIC



ELECTRICAL CHARACTERISTICS @ 25°C



Turns Ratio
T x SIDE: 1CT : 1CT

DCL 350μH MIN.
@100KHz, 100mV, WITH 8mA DC BIAS.

PIN(2-3), (5-6), (8-9), (11-12)
PIN(14-15), (17-18), (20-21), (23-24)

Insertion Loss -1.0dB MAX. @ 1MHz TO 100MHz

Return Loss -18dB MIN. @ 1-30MHz
MEASURED UNDER A -14.4dB MIN. @ 40MHz
100 Q ENVIRONMENT -13.1dB MIN. @ 50MHz

-12dB MIN. @ 60-80MHz
-10dB MIN. @ 80-100MHz

-35dB MIN. AT 1MHz TO 100MHz

COMMON TO COMMON
MODE REJECTION -30dB MIN. AT 1MHz TO 100 MHz

DCMR -33dB MIN. AT 1MHz TO 100 MHz

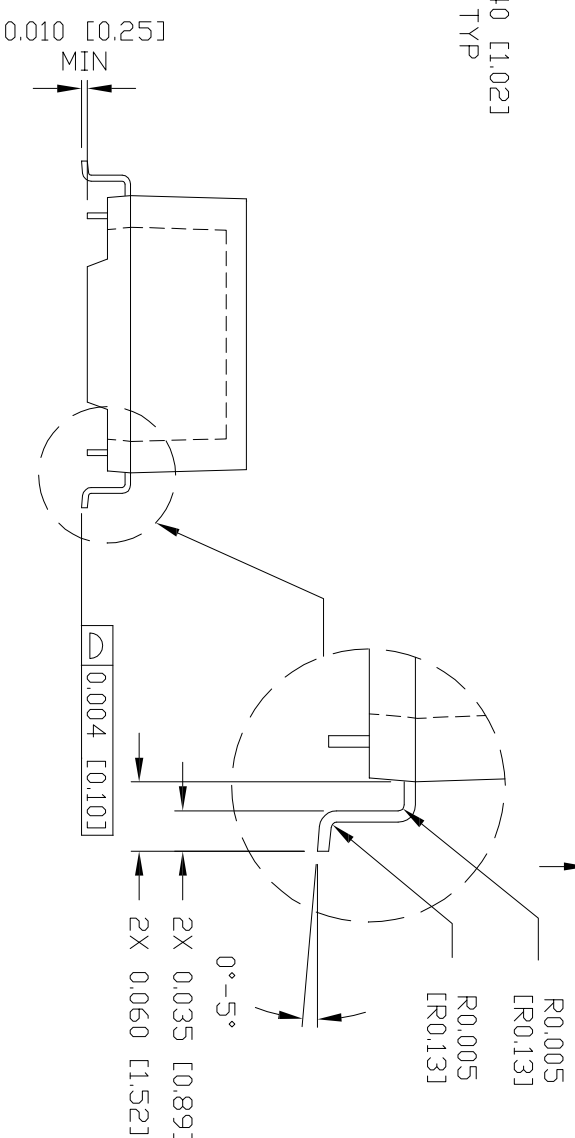
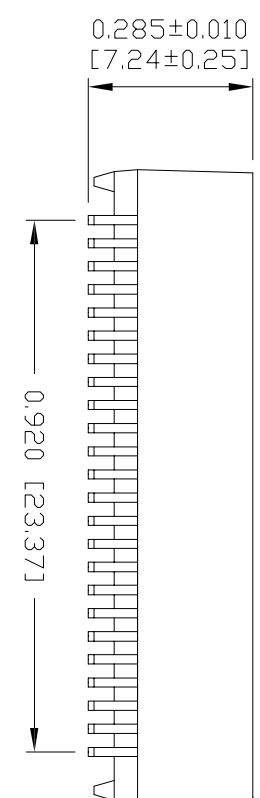
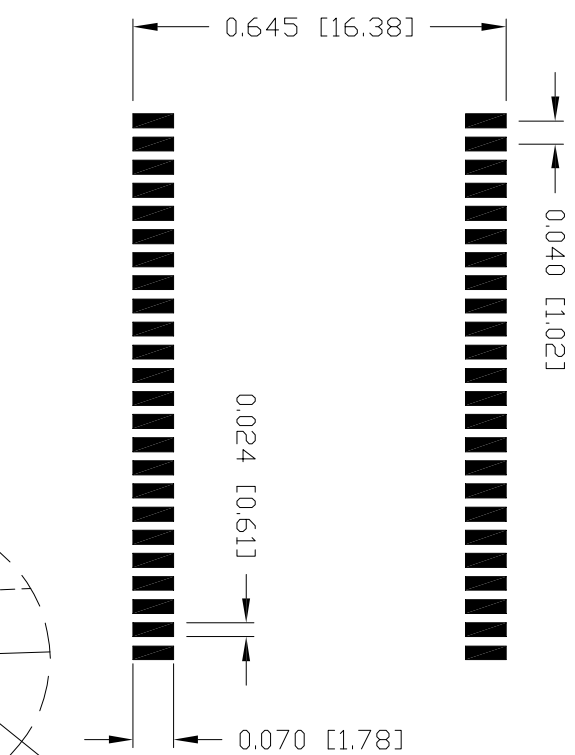
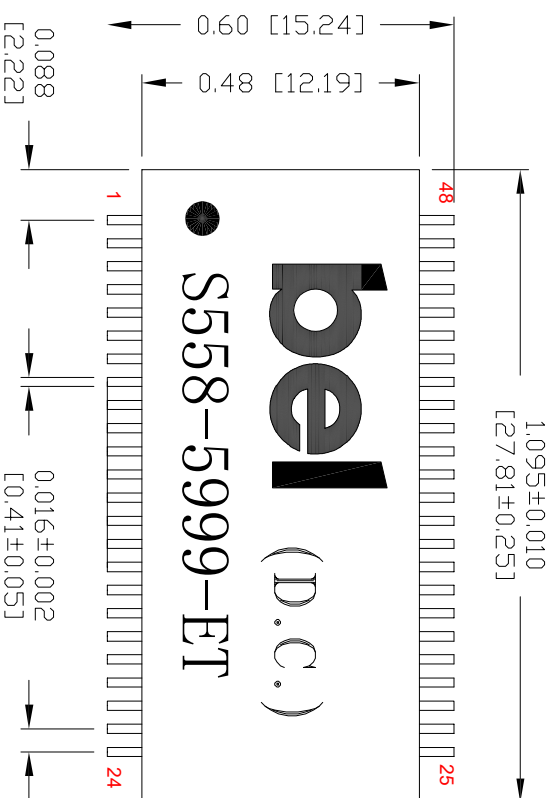
HIPOT LEAKAGE CURRENT LESS THAN 1mA A-I 1500VAC

Applicable Operating temp 0°C~+70°C

ORIGINATED BY	DATE	TITLE			PART NO. / DRAWING NO.		STANDARD DIM.		[] METRIC DIM. AS REF.		 POWER PROTECT CONNECT
yan bin zhang	2025-2-27	Electrical specification			S558-5999-ET		TOL. IN INCH		UNIT : INCH [mm]		
DRAWN BY	DATE	DUAL 1000BASE-T module			FILE NAME		.X		SCALE : N/A		
ya chen zhou	2025-2-27				S558-5999-ET_A.DWG		.XX				
							.XXX				

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SUGGESTED PCB PAD LAYOUT



ORIGINATED BY	DATE	TITLE		PART NO. / DRAWING NO.		STANDARD DIM.		[] METRIC DIM. AS REF.		 POWER PROTECT CONNECT
yan bin zhang	2025-2-27	Electrical specification		S558-5999-ET		TOL. IN INCH		UNIT : INCH [mm]		
DRAWN BY	DATE	DUAL 1000BASE-T module		FILE NAME		.X		SCALE : N/A		
ya chen zhou	2025-2-27			S558-5999-ET_A.DWG		.XX		SIZE : A0		
						.XXX				