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ELECTRICAL SPECIFICATIONS @25°C

RoHS



TURNS RATIO

1CT : 1CT

PHASE

2 TD 23, 5 TD 20,
8 TD 17, 11 TD 14 IN PHASE

DCL

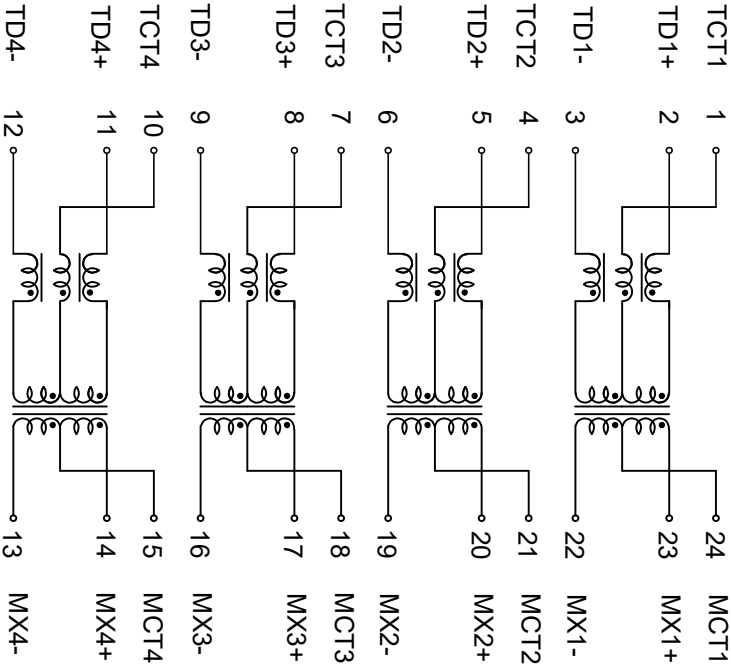
AT 100kHz, 100mV

<23-22>, <20-19>, <17-16>, <14-13>

350uH MIN. WITH 8mA DC BIAS
120uH MIN. WITH 19mA DC BIAS

CHIP Side
(Primary) Line Side
(Secondary)

SCHEMATIC



D.C.R
<2-3>, <5-6>, <8-9>, <11-12>

1.2DHM Max

INSERTION LOSS

-1.0dB MAX. @ 1 TD 100MHZ
-2.0dB MAX. @ 125MHZ

RETURN LOSS

-18dB MIN. @ 1-30MHZ
-13.1dB MIN. @ 60MHZ

MEASURED UNDER A 100 Ω I/D

-12dB MIN. @ 80MHZ
-10dB MIN. @ 100MHZ

CROSSSTALK

-30dB MIN. AT 10 TD 100 MHZ
-30dB MIN. AT 100kHz TD 100MHZ

COMMON TD COMMON REJECTION

LEAKAGE CURRENT LESS THAN 1mA AT 1500VAC

HIPOT

PoE

Suitable for End-span PoE applications
with 720mA current capability

Applicable Operating temp

-40°C~+85°C

ORIGINATED BY	DATE	TITLE	PART NO. / DRAWING NO.		STANDARD DIM.	[] METRIC DIM. AS REF.		REV. : A	PAGE : 2
yan bin zhang	2025-5-16	Electrical specification	S558-5500-JZ		TOL. IN INCH	UNIT : INCH [mm]			
DRAWN BY	DATE	1000BT module	FILE NAME		.X		SCALE : N/A		
					.XX				
ya chen zhou	2025-5-16		S558-5500-JZ_A.DWG		.XXX			SIZE : A1	



POWER | PROTECT | CONNECT

