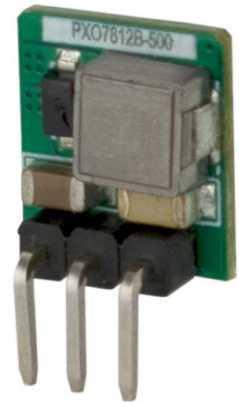


SERIES: PX078B-500 | DESCRIPTION: NON-ISOLATED DC SWITCHING REGULATOR

FEATURES

- 0.5 A current output
- pin compatible with LM78XX linear regulators
- input voltage range up to 4.75 ~ 36 Vdc
- continuous short circuit protection
- compact open-frame design
- certified to EN/IEC 62368-1

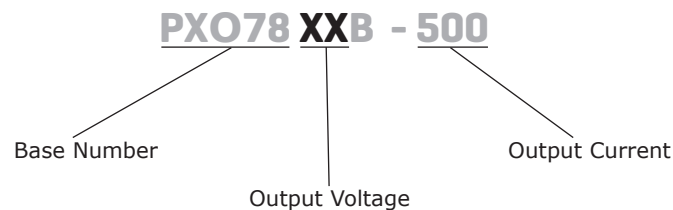


MODEL

	input voltage ¹		output voltage (Vdc)	output current max (mA)	output power max (W)	ripple & noise ² max (mVp-p)	efficiency typ (%)
	typ (Vdc)	range (Vdc)					
PX07803B-500	24	4.75~36	3.3	500	1.65	100	92
PX07805B-500	24	6.5~36	5	500	2.5	100	93 80
	12	7~31	-5	-300	0.99		
PX07809B-500	24	12~36	9	500	4.5	100	93
PX07812B-500	24	15~36	12	500	6.0	100	94 84
	12	8~24	-12	-150	1.8		
PX07815B-500	24	19~36	15	500	7.5	100	95 85
	12	8~21	-15	-150	2.25		

Notes: 1. For input voltages higher than 30 Vdc, a 22 μ F / 50 V input capacitor is required.
 2. Tested at nominal input, 10~100%, 20 MHz bandwidth.
 3. All specifications are measured at Ta=25°C, humidity < 75%, nominal input voltage, and rated output load unless otherwise specified.

PART NUMBER KEY



INPUT

parameter	conditions/description	min	typ	max	units
input reverse polarity protection	no				
no-load input current	positive output at nominal input voltage		0.5	1.5	mA
filter	capacitor filter				

OUTPUT

parameter	conditions/description	min	typ	max	units
maximum capacitive load	positive output			680	μF
	negative output			330	μF
voltage accuracy	at full load, input voltage range		±2	±4	%
	3.3 Vdc output model		±2	±3	%
	all other models				
line regulation	at full load, input voltage range		±0.2	±0.4	%
load regulation	at nominal input, 10~100% load		±0.6		%
	3.3, ±5 Vdc output models		±0.3		%
	all other models				
switching frequency	at nominal input voltage, full load		500		kHz
transient recovery time	at nominal input voltage, 25% load step change		0.2	1	ms
transient response deviation	at nominal input voltage, 25% load step change		50	250	mV
temperature coefficient	operating temperature -40°C~85°C			±0.03	%/°C

PROTECTIONS

parameter	conditions/description	min	typ	max	units
short circuit protection	continuous, auto recovery				

SAFETY AND COMPLIANCE

parameter	conditions/description	min	typ	max	units
safety approvals	certified to 62368-1: EN, IEC				
conducted emissions	CISPR32/EN55032 CLASS B (see Fig. 3-2 for recommended circuit)				
radiated emissions	CISPR32/EN55032 CLASS B (see Fig. 3-2 for recommended circuit)				
ESD	IEC/EN 61000-4-2 Contact ±4kV, perf. Criteria B				
radiated immunity	IEC/EN 61000-4-3 10V/m, perf. Criteria A				
EFT/burst	IEC/EN 61000-4-4 ±1kV (see Fig. 3-1 for recommended circuit), perf. Criteria B				
conducted immunity	IEC/EN 61000-4-6 3 Vrms, perf. Criteria A				
MTBF	as per MIL-HDBK-217F, 25°C	2,000,000			hours
RoHS	yes				

ENVIRONMENTAL

parameter	conditions/description	min	typ	max	units
operating temperature	see derating curve	-40		85	°C
storage temperature		-55		125	°C
storage humidity	non-condensing	5		95	%

SOLDERABILITY

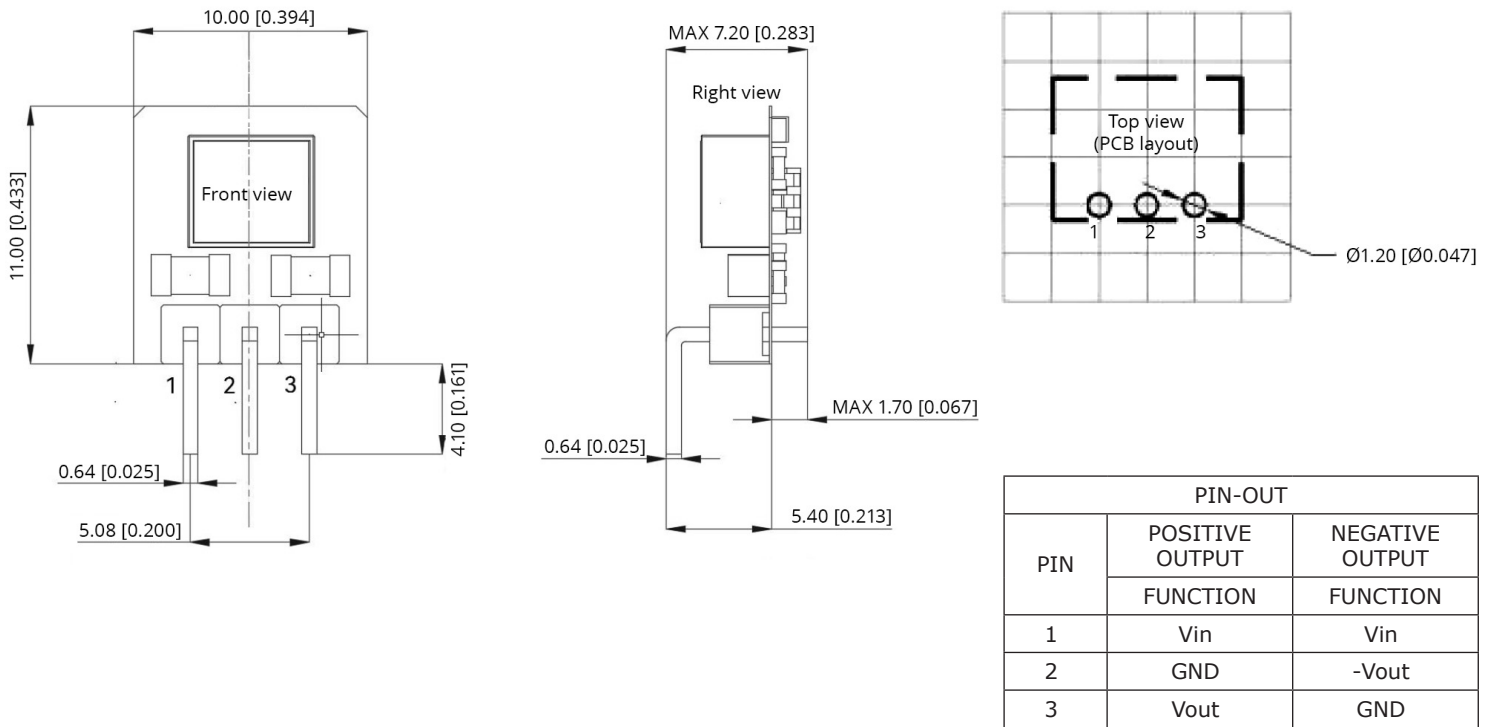
parameter	conditions/description	min	typ	max	units
wave soldering resistance temperature	soldering time: 5~10 seconds	255	260	265	°C
hand soldering resistance temperature	soldering time: 3~5 seconds	350	360	370	°C

MECHANICAL

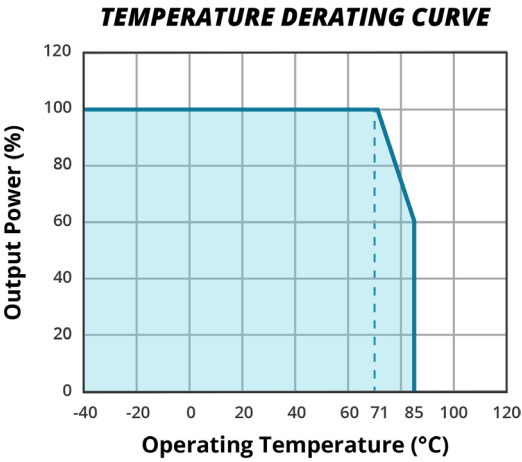
parameter	conditions/description	min	typ	max	units
dimensions	10.00 x 7.20 x 11.00 [0.394 x 0.284 x 0.433 inch]				mm
weight			1.1		g
cooling method	natural convection				

MECHANICAL DRAWING

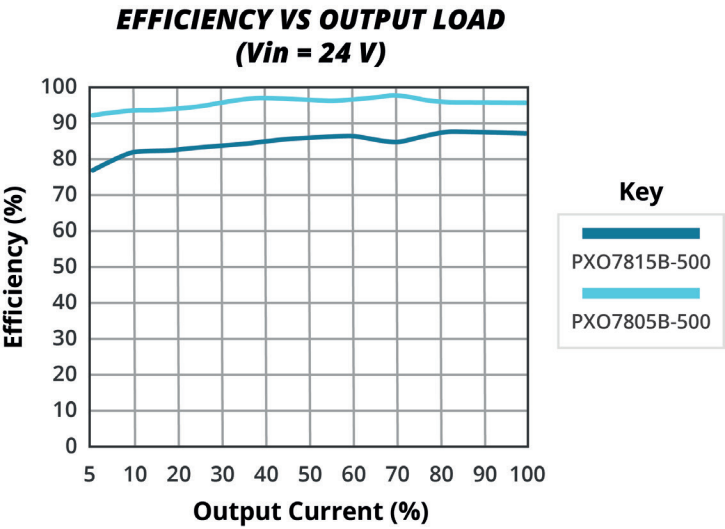
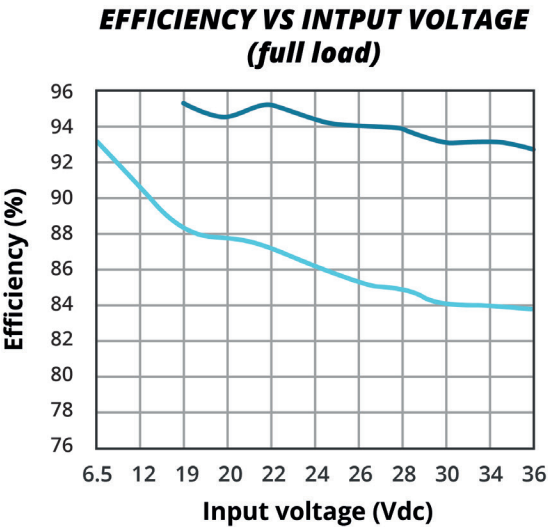
units: mm [inch]
tolerance: ±0.50[±0.020]
pin section tolerance: ±0.10[±0.004]



DERATING CURVE



EFFICIENCY CURVES



TYPICAL APPLICATION CIRCUIT

Figure 1

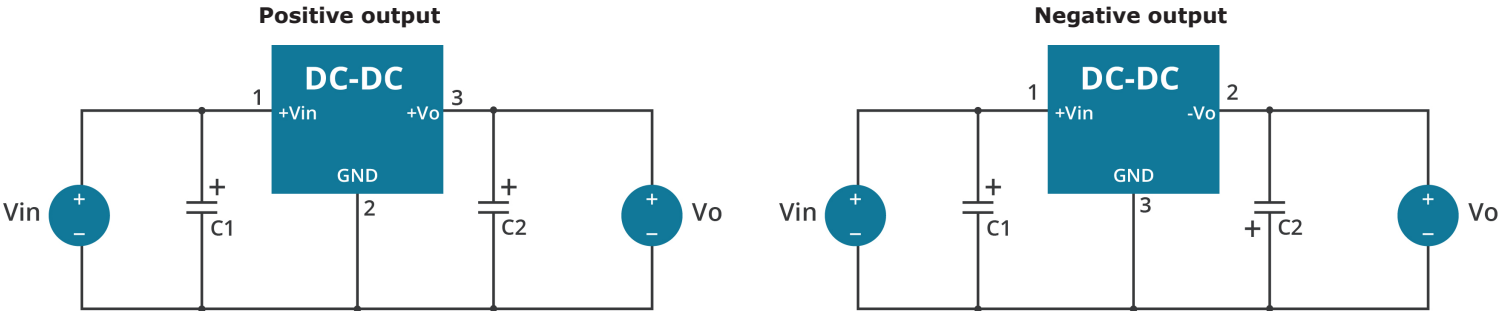


Figure 2

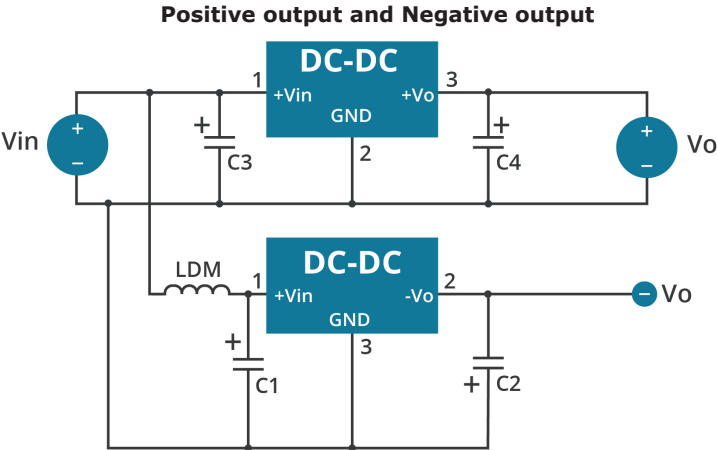


Table 1

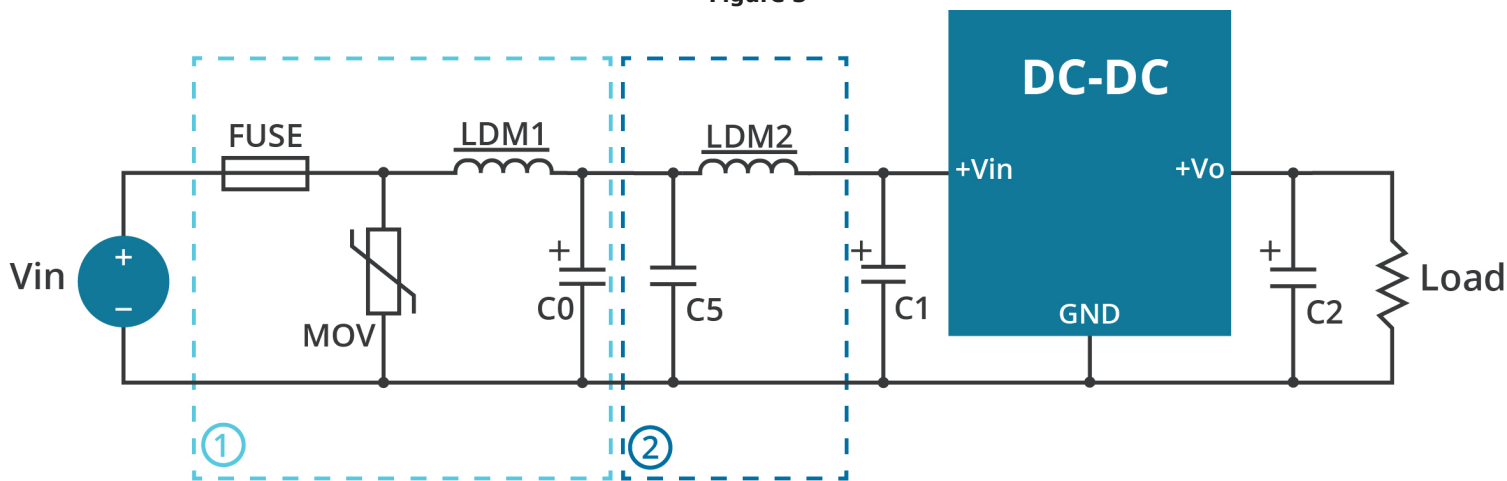
Model Number	C1/C3 (ceramic capacitor)	C2/C4 (ceramic capacitor)
PX07803B-500	10 μ F/50 V	22 μ F/10 V
PX07805B-500	10 μ F/50 V	22 μ F/10 V
PX07809B-500	10 μ F/50 V	22 μ F/25 V
PX07812B-500	10 μ F/50 V	22 μ F/25 V
PX07815B-500	10 μ F/50 V	22 μ F/25 V

Note:

1. The required capacitors C1 and C2 (C3 and C4) must be connected as close as possible to the terminals of the module.
2. Refer to Table 1 for C1 and C2 (C3 and C4) capacitor values. For certain applications, increased values and/or tantalum or low ESR electrolytic capacitors may also be used instead.
3. Converter cannot be used for hot swap and with output in parallel.
4. When using the configuration shown in Fig. 2, we recommend adding an inductor (LDM) with a value of 10~47 μ H to help reduce mutual interference.

EMC RECOMMENDED CIRCUIT

Figure 3



Note: For EMC tests we use Part ① in Fig. 5 for immunity and part ② for emissions test. Selecting based on needs.

Table 2

Recommended external circuit components	
FUSE	choose according to actual input current
MOV	20D470K
LDM1	82 μ H
C0	680 μ F/50 V
C1 / C2	see Table 1
C5	4.7 μ F/50 V
LDM2	12 μ H

REVISION HISTORY

rev.	description	date
1.0	initial release	08/17/2026

The revision history provided is for informational purposes only and is believed to be accurate.



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CUI offers a two (2) year limited warranty. Complete warranty information is listed on our website.

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