SERIES: VEFT1-D-SMT | **DESCRIPTION:** DC-DC CONVERTER

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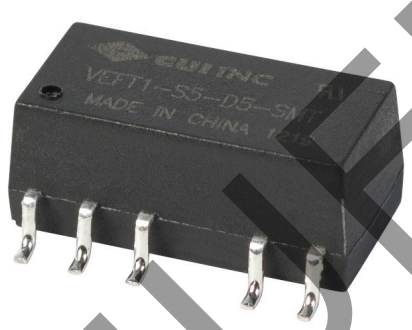
FEATURES

- 1 W isolated output
- industry standard 12 pin SMT package
- dual unregulated outputs
- 3,000 V isolation
- short circuit protection
- UL safety approvals (some models)
- wide temperature (-40~85°C)
- efficiency up to 80%



MODEL

	input voltage		output voltage	output current		output power	ripple and noise[1]	efficiency
	typ (Vdc)	range (Vdc)	(Vdc)	min (mA)	max (mA)	max (W)	max (mVp-p)	typ (%)
VEFT1-S5-D12-SMT	5	4.5~5.5	±12	±5	±42	1	75	78
VEFT1-S12-D9-SMT	12	10.8~13.2	±9	±6	±56	1	75	76
VEFT1-S12-D12-SMT	12	10.8~13.2	±12	±5	±42	1	75	78
VEFT1-S12-D15-SMT	12	10.8~13.2	±15	±4	±33	1	75	79
VEFT1-S24-D5-SMT	24	21.6~26.4	±5	±10	±100	1	75	79
VEFT1-S24-D9-SMT	24	21.6~26.4	±9	±6	±56	1	75	80
VEFT1-S24-D12-SMT	24	21.6~26.4	±12	±5	±42	1	75	80
VEFT1-S24-D15-SMT	24	21.6~26.4	±15	±4	±33	1	75	80

Notes: 1. ripple and noise are measured at 20 MHz BW

PART NUMBER KEY

VEFT1 - SXX - DXX -SMT - X

- VEFT1 — Base Number
- SXX — Input Voltage
- DXX — Output Voltage
- SMT — Packaging Style
- X — Package Options "blank" = standard, TR = Tape & Reel

Please read our datasheet & drawing disclaimer here.

INPUT

parameter	conditions/description	min	typ	max	units
operating input voltage	5 V model	4.5	5	5.5	Vdc
	12 V model	10.8	12	13.2	Vdc
	24 V model	21.6	24	26.4	Vdc

OUTPUT

parameter	conditions/description		min	typ	max	units
line regulation	for Vin change of 1%				±1.2	%
load regulation	measured from 10% load to full load	5 V model		12.8	15	%
		9 V model		8.3	10	%
		12 V model		6.8	10	%
		15 V model		6.3	10	%
voltage accuracy	see derating curves					
switching frequency	100% load, 5 and 12 V input			100		kHz
	100% load, 24 V input			500		kHz
temperature coefficient				±0.03		%/°C

PROTECTIONS

parameter	conditions/description	min	typ	max	units
short circuit protection				1	s

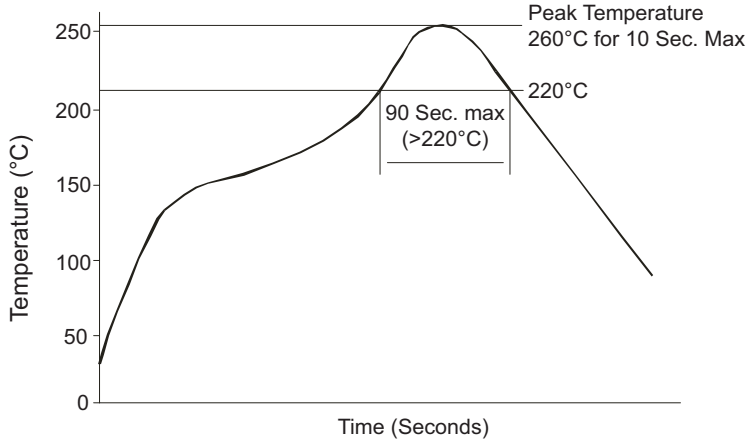
SAFETY AND COMPLIANCE

parameter	conditions/description	min	typ	max	units
isolation voltage	for 1 minute at 1 mA max.	3,000			Vdc
isolation resistance	at 500 Vdc	1,000			MΩ
safety approvals[1]	UL				
MTBF		3,500,000			hours
RoHS compliant	yes				

Notes: 1. VEFT1-S5-D12 and VEFT1-S12-D9/12/15 only

ENVIRONMENTAL

parameter	conditions/description	min	typ	max	units
operating temperature		-40		85	°C
storage temperature		-55		125	°C
storage humidity	non-condensing			95	%
temperature rise	at full load		15	25	°C
reflow soldering	see reflow soldering profile			260	°C



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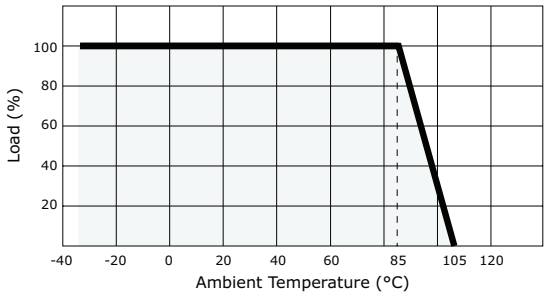
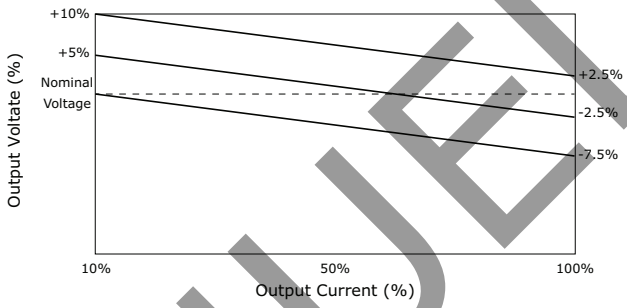
DERATING CURVES

1. output power vs. ambient temperature



2. output voltage vs. output current

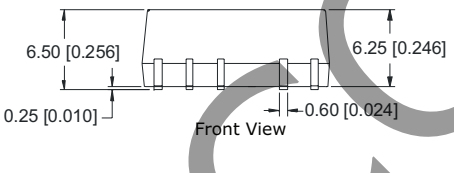


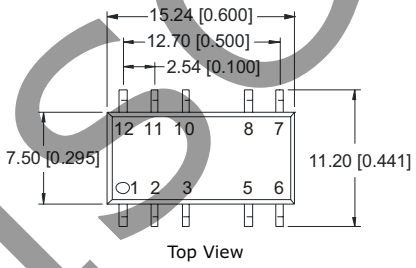
MECHANICAL

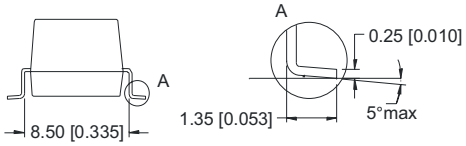
parameter	conditions/description	min	typ	max	units
dimensions	15.24 x 11.20 x 6.50 (0.600 x 0.441 x 0.256 inch)				mm
case material	plastic (UL94-V0)				
weight			1.71		g

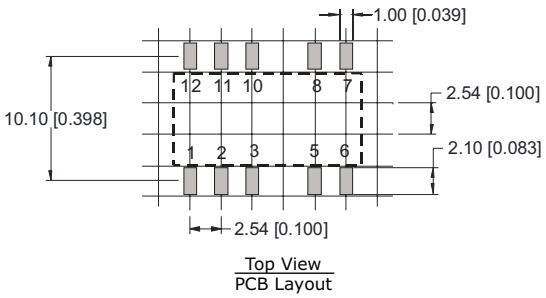
MECHANICAL DRAWING

units: mm [inches]
tolerance: ±0.25 [±0.010]
pin section tolerance: ±0.10 mm [±0.004]









PIN CONNECTIONS	
PIN	FUNCTION
1	GND
2	+Vin
5	0 V
6	-Vo
8	+Vo
others	NC

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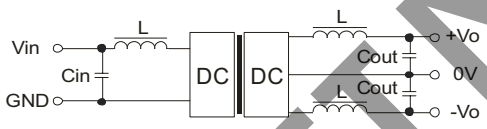
APPLICATION NOTES

1. **Requirement on Output Load**
   In order to ensure the product operates efficiently and reliably, make sure the specified range of input voltage is not exceeded and the minimum output load is not less than 10% load. If the actual load is less than the specified minimum load, the output ripple may increase sharply while its efficiency and reliability will reduce greatly. If the actual output power is very small, please add an appropriate resistor as extra loading.

2. **Overload Protection**
   Under normal operating conditions, the output circuit of these products has no protection against over-current and short-circuits. The simplest method is to connect a self-recovery fuse in series at the input end or add a circuit breaker to the circuit.

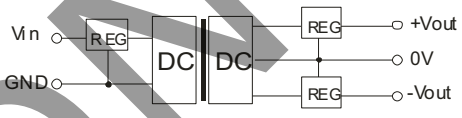
3. **Filtering**
   In some circuits which are sensitive to noise and ripple, a filtering capacitor may be added to the DC/DC output end and input end to reduce the noise and ripple. However, the capacitance of the output filter capacitor must be proper. If the capacitance is too big, a startup problem might arise. For every channel of output, provided the safe and reliable operation is ensured, the greatest capacitance of its filter capacitor sees the external capacitor table. To get an extremely low ripple, an "LC" filtering network may be connected to the input and output ends of the DC/DC converter, which may produce a more significant filtering effect. It should also be noted that the inductance and the frequency of the "LC" filtering network should be staggered with the DC/DC frequency to avoid mutual interference (Figure 1).

Figure 1



4. **Output Voltage Regulation and Over-voltage Protection Circuit**
   The simplest device for output voltage regulation, over-voltage and over-current protection is a linear voltage regulator with overheat protection that is connected to the input or output end in series (Figure 2).

Figure 2



5. **External Capacitor Table**
   It is not recommended to connect any external capacitor in the application field with less than 0.5 W output.

Table 1

Vin (Vdc)	Cin (µF)	Vout (Vdc)	Cout (µF)
5	4.7	±5	4.7
12	2.2	±9	2.2
24	1	±12	1
--	--	±15	1

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Note: All specifications measured at 25°C, humidity <75%, nominal input voltage, and full load unless otherwise noted.

REVISION HISTORY

rev.	description	date
1.0	initial release	04/09/2009
1.01	new template applied	04/12/2012
1.02	V-Infinity branding removed	09/05/2012
1.03	added TR package option	11/01/2012
1.04	discontinued VEFT1-S5-D5-SMT, VEFT1-S5-D9-SMT, VEFT1-S5-D15-SMT, and VEFT1-S12-D5-SMT models	07/01/2013

The revision history provided is for informational purposes only and is believed to be accurate.

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