

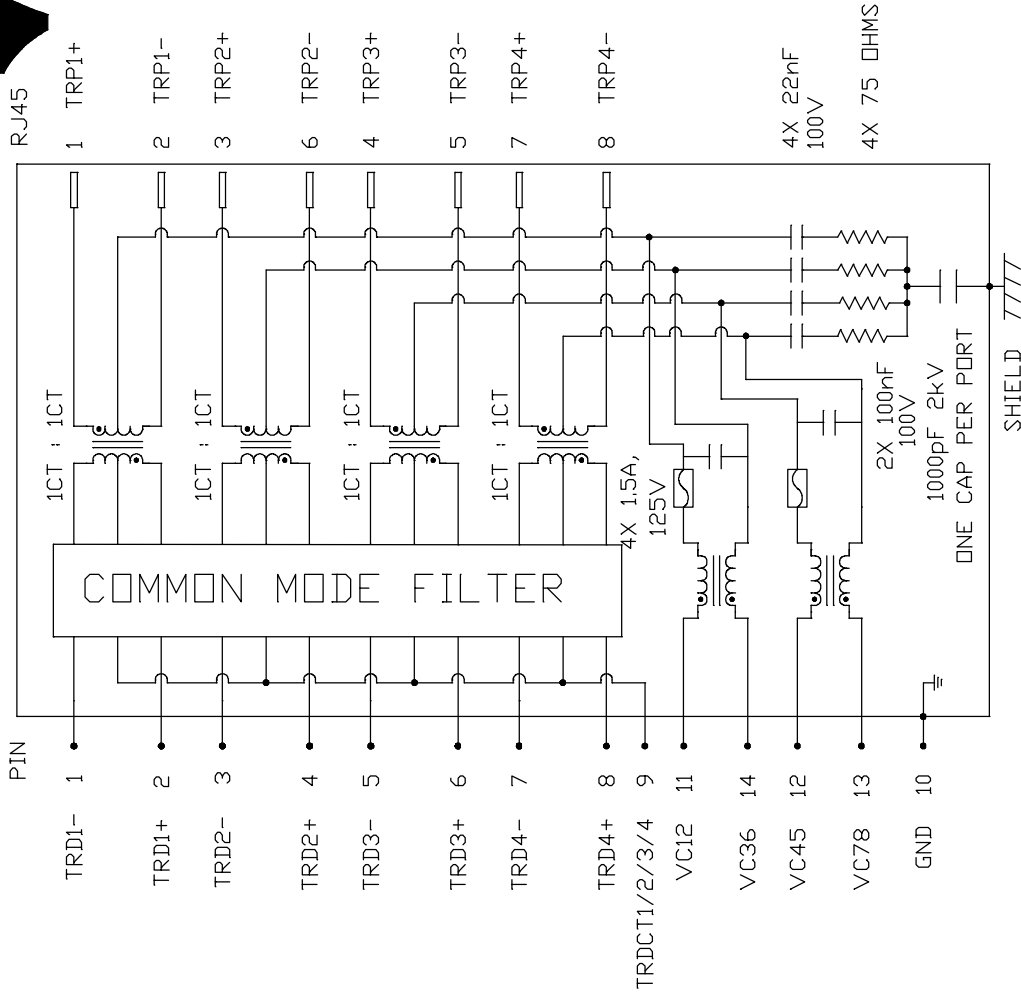
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ELECTRICAL CHARACTERISTICS @ 25°C

- TURNS RATIO
 TP1 1CT : 1CT ±2%
 TP2 1CT : 1CT ±2%
 TP3 1CT : 1CT ±2%
 TP4 1CT : 1CT ±2%
 DCL @ 100kHz/100mVRMS 180µH MIN.
 (-40°C - +85°C) 120µH MIN.
 19mA DC BIAS (-40°C - +85°C)
 INS. LOSS
 1MHZ TO 150MHZ -0.004(f/MHZ)-0.4 dB MAX
 RET. LOSS
 1MHZ-40MHZ -20 dB MIN
 40.1MHZ-150MHZ -20+15LOG(f/40MHZ) dB MIN
 CROSSTALK
 1MHZ - 40MHZ -35 dB MIN
 40MHZ-150MHZ -35+15LOG(f/40MHZ) dB MIN
 CM TO CM REJ
 1MHZ - 150MHZ -25 dB MIN
 HIPOT (Isolation Voltage): 2250 VDC
 100% OF PRODUCTION TESTED TO COMPLY WITH IEEE 802.3 ISOLATION REQUIREMENTS.
 NOTE: PORT-TO-PORT ISOLATION NOT INCLUDED.
 BALANCED DC LINE CURRENT 600mA MAX. CONTINUOUS
 1.2A MAX. FOR 200 MILLISECONDS
 OPERATING TEMPERATURE: -40°C TO +85°C

RoHS

SCHEMATIC



ORIGINATED BY CHOW WANCHUNG	TITLE 2X8 2.5 Gigabit MagJack®
DATE 2021-04-14	4PPoE(EXTENDED TEMPERATURE) 0854-2X8J-JDB-F
DRAWN BY TANG YUAN	PATENTED
DATE 2021-04-14	

PART NO. / DRAWING NO. 08542X8JJDBF	STANDARD DIM. TOL. IN INCH	[] METRIC DIM. AS REF.
FILE NAME 08542X8JJDBF_A.DWG	.X	UNIT : INCH [mm]
	.XX	SCALE : N/A
	.XXX	SIZE : A4

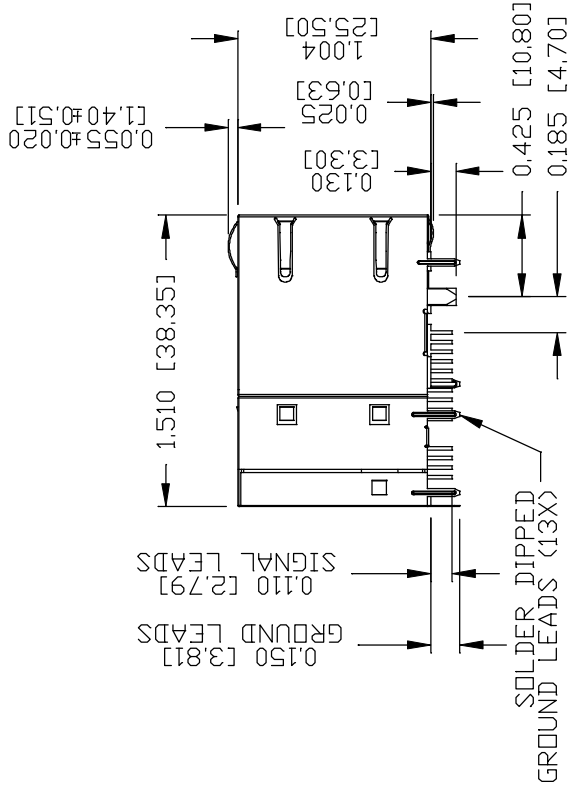
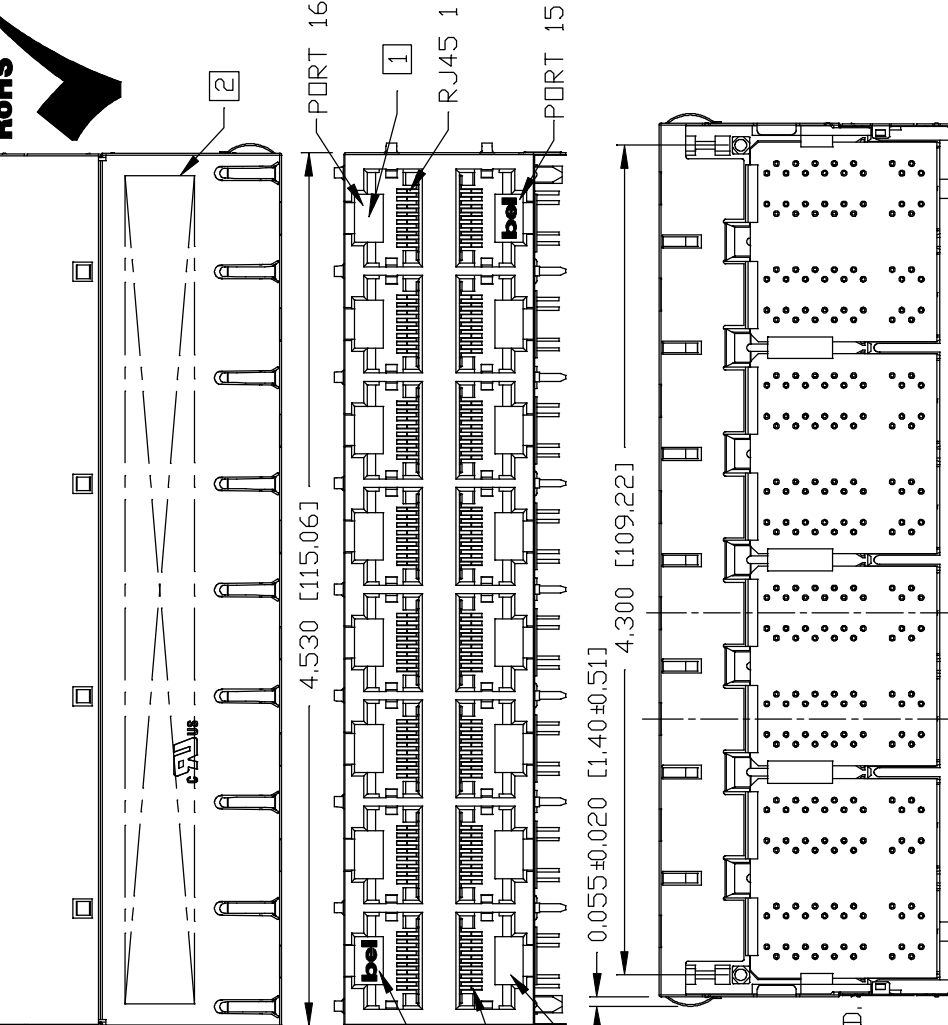
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MAGNETIC SOLUTIONS
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NOTES:


- 1. JACK CAVITY CONFORMS TO FCC RULES AND REGULATIONS, PART 68 SUBPART F.
- 2. MARK PART WITH MFG LOGO, MFG NAME, PART NUMBER, DATE CODE AND PATENTED.
- 3. THE PRODUCT IS RoHS COMPLIANT.
- 4. THE PRODUCT IS PATENTED - FILE #E196366 AND E169987.
- 5. THE PART IS RECOMMENDED FOR WAVE SOLDERING. THE SUGGESTED PEAK WAVE SOLDERING CONDITION IS 260°C MAX AND 10 SECONDS MAX.

PLASTIC HOUSING: THERMOPLASTIC PBT, BLACK
 FLAMMABILITY RATING UL 94V-0
 CONTACT PLATING: 50 MICRO-INCH HARD GOLD PLATING OR EQUIVALENT.
 OUTPUT PINS: TIN-COATED COPPER WIRE, DIA 0.018 INCH.
 100 MICRO-INCH MIN MATTE TIN. PINS ARE SOLDER DIPPED.
 METAL SHIELD: NICKEL PLATED ON COPPER ALLOY.
 (ALL GROUND LEADS ARE SOLDER DIPPED)

MARK PART WITH MFG LOGO, MFG NAME, PART NUMBER, DATE CODE AND PATENTED.

THE PRODUCT IS RoHS COMPLIANT.

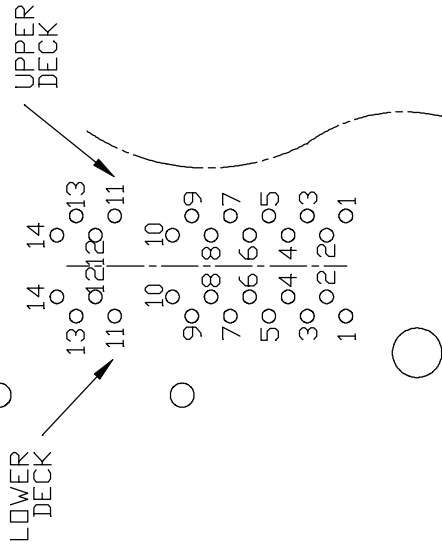
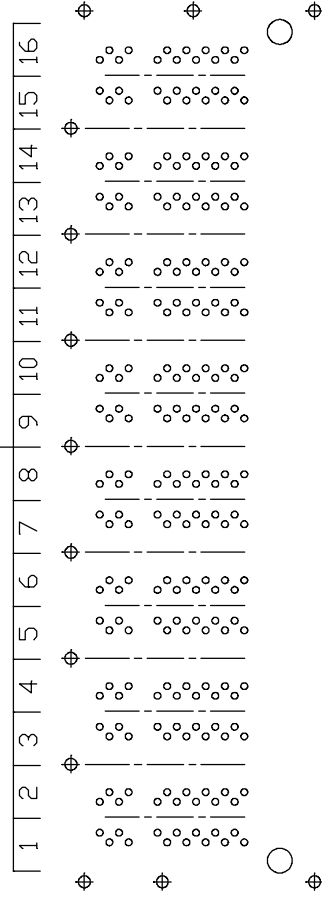
THE PRODUCT IS PATENTED, THE PATENT NUMBER ARE U.S. PAT. 6,840,817 AND U.S. PAT. 7,123,117.

ORIGINATED BY SANDY LIN		PART NO. / DRAWING NO. 08542X8JJDDBF		STANDARD DIM. TOL. IN INCH		<input type="checkbox"/> METRIC DIM. AS REF.		REV. : A PAGE : 3	
DATE 2021-04-14		FILE NAME 08542X8JJDDBF_A.DWG		TOL. IN INCH .X .XX .XXX		UNIT : INCH [mm] SCALE : N/A		 MAGNETIC SOLUTIONS a bel group	
DRAWN BY RUAN WENJUN		TITLE 2X8 2.5 Gigabit MagJack® 4PPoE(EXTENDED TEMPERATURE) 0854-2X8J-JDB-F PATENTED		SIZE : A4					
DATE 2021-04-14									

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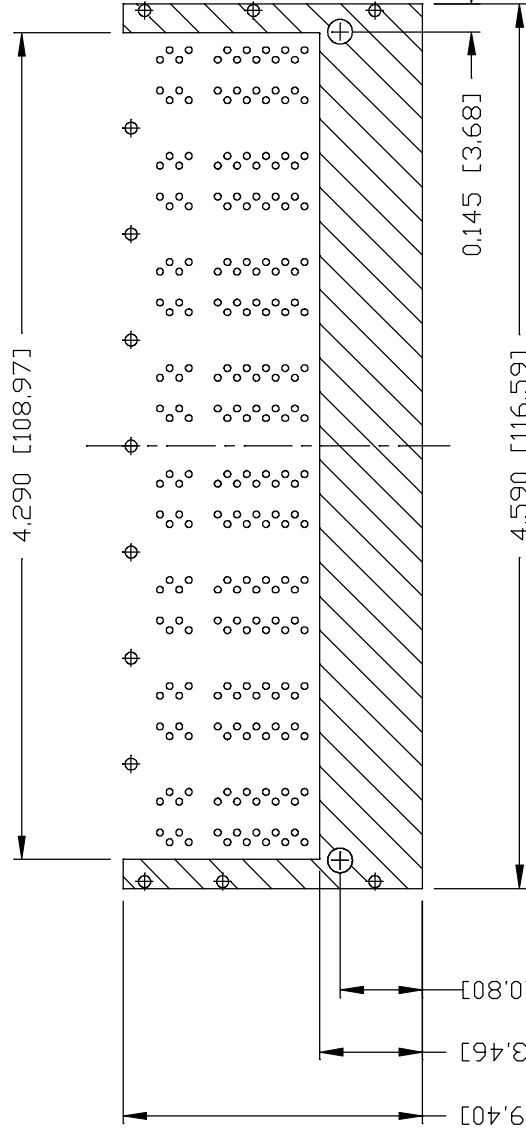


PORT ASSIGNMENT
(COMPONENT SIDE VIEW)



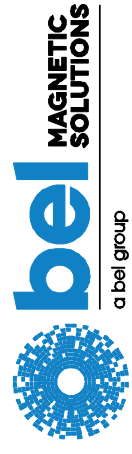
PIN-OUT INFORMATION
(COMPONENT SIDE VIEW)

14	VC36
13	VC78
12	VC45
11	VC12
10	GND
9	TRDCT1/2/3/4
8	TRD 4+
7	TRD 4-
6	TRD 3+
5	TRD 3-
4	TRD 2+
3	TRD 2-
2	TRD 1+
1	TRD 1-



THE SHADED AREA ON THE CUSTOMER BOARD ARE RECOMMENDED TO BE CLEAR OFF ANY VIA HOLE OR COMPONENT PAD.

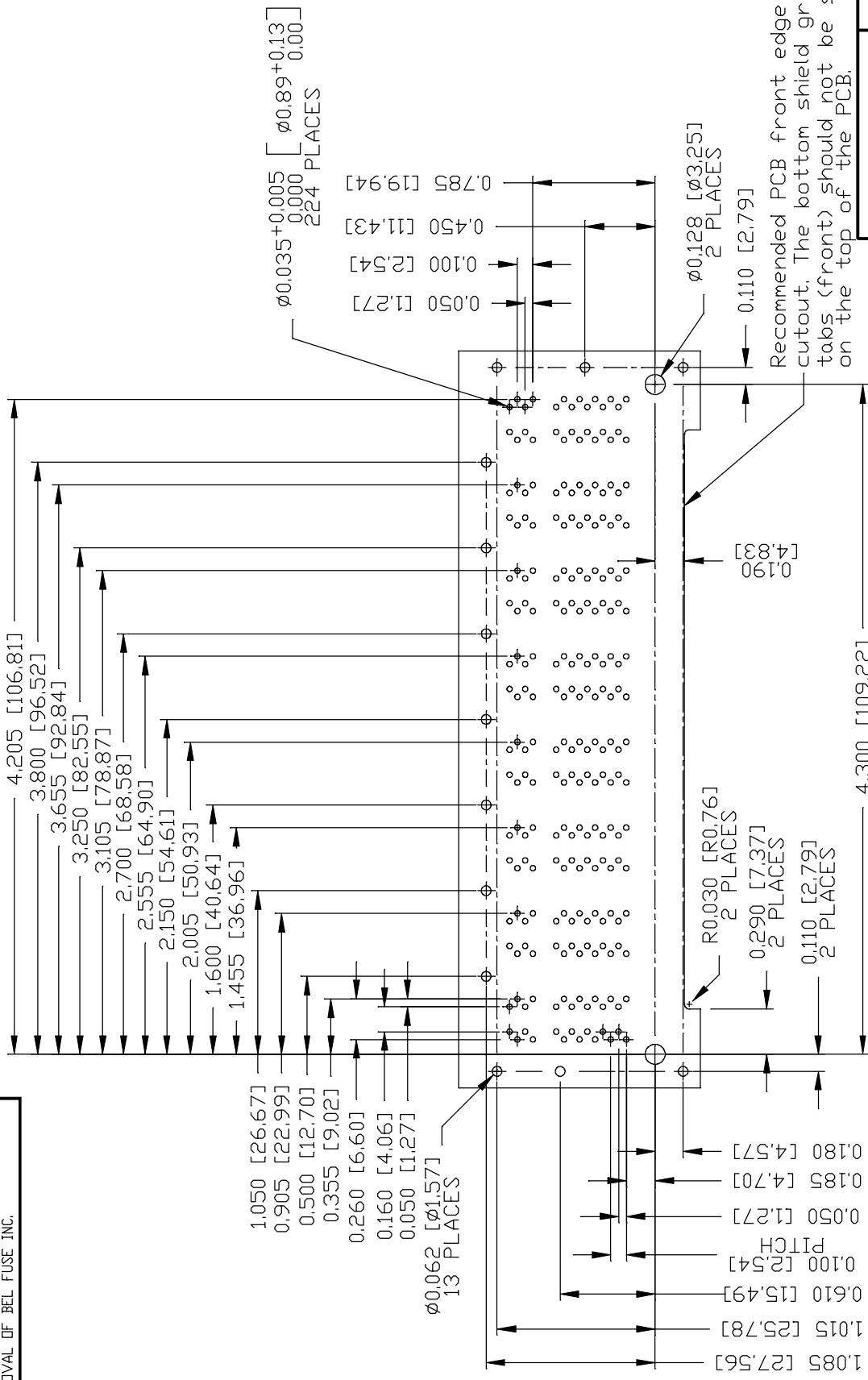
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PART NO. / DRAWING NO.		08542X8JDBF	
FILE NAME		08542X8JDBF_A.DWG	
TITLE		2X8 2.5 Gigabit MagJack® 4PPoE(EXTENDED TEMPERATURE) 0854-2X8J-JDB-F PATENTED	
ORIGINATED BY	SANDY LIN		
DATE	2021-04-14		
DRAWN BY	RUAN WENJUN		
DATE	2021-04-14		



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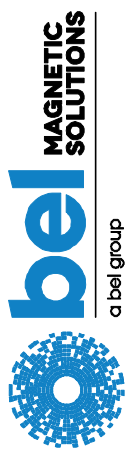
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SUGGESTED FOOTPRINT
(COMPONENT SIDE VIEW)



Recommended PCB front edge cutout. The bottom shield ground tabs (front) should not be seated on the top of the PCB.

REV. : A		PAGE : 5	
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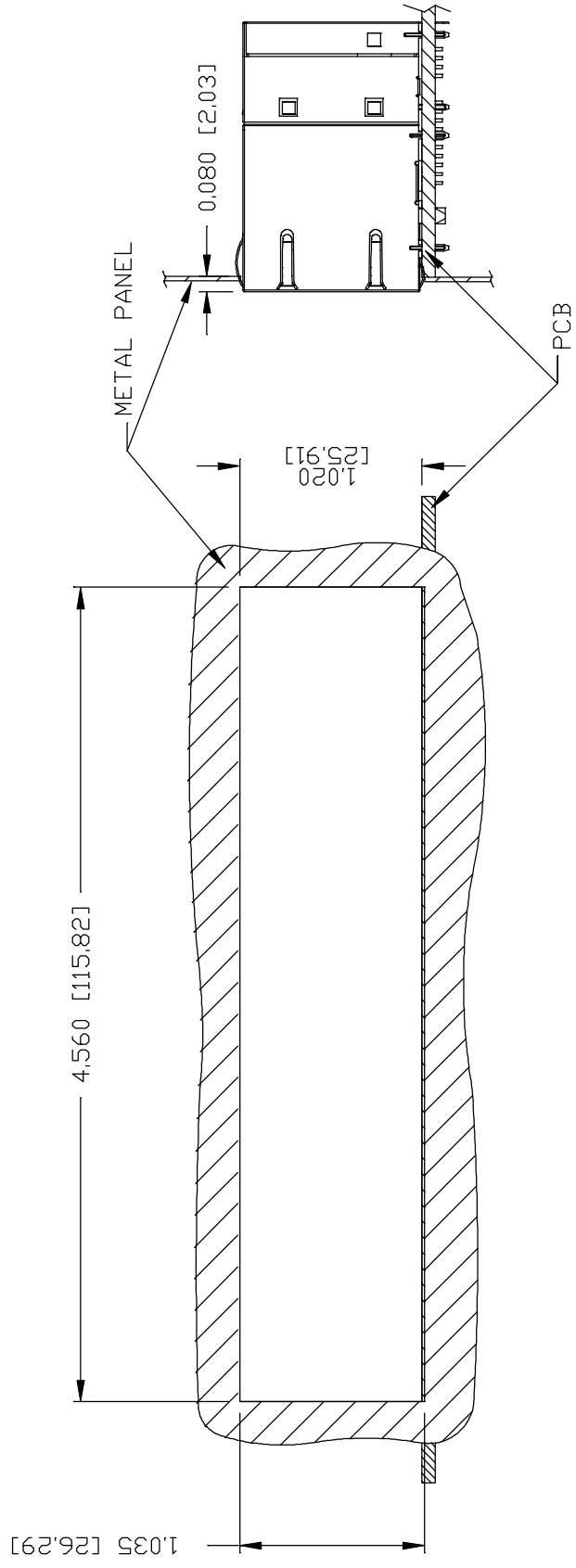


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SUGGESTED PANEL OPENING



NOTE:

THE DISTANCE OF PANEL INSIDE SURFACE RELATIVE TO FRONT SURFACE OF PART IS ONLY A SUGGESTION. IN CASE THIS DISTANCE IS DIFFERENT, THE REQUIRED PANEL OPENING DIMENSIONS CHANGE ACCORDINGLY.

PACKING INFORMATION

PACKING TRAY : 0200-9999-D1 (TOP)
0200-9999-D2 (BOTTOM)

PACKING QUANTITY : 10 PCS FINISHED GOODS PER TRAY.
5 TRAYS (50 PCS FINISHED GOODS) PER CARTON BOX.

NOTE : CARDBOARDS ARE PLACED BETWEEN LAYERS OF PACKING TRAY INSIDE CARTON BOX. (INCLUDE THE UPPERMOST AND LOWERMOST TRAY)

REV. : A		PAGE : 6	
ORIGINATED BY SANDY LIN DATE 2021-04-14 DRAWN BY RUAN WENJUN DATE 2021-04-14		TITLE 2X8 2.5 Gigabit MagJack® 4PPoE(EXTENDED TEMPERATURE) 0854-2X8J-JDB-F PATENTED	
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