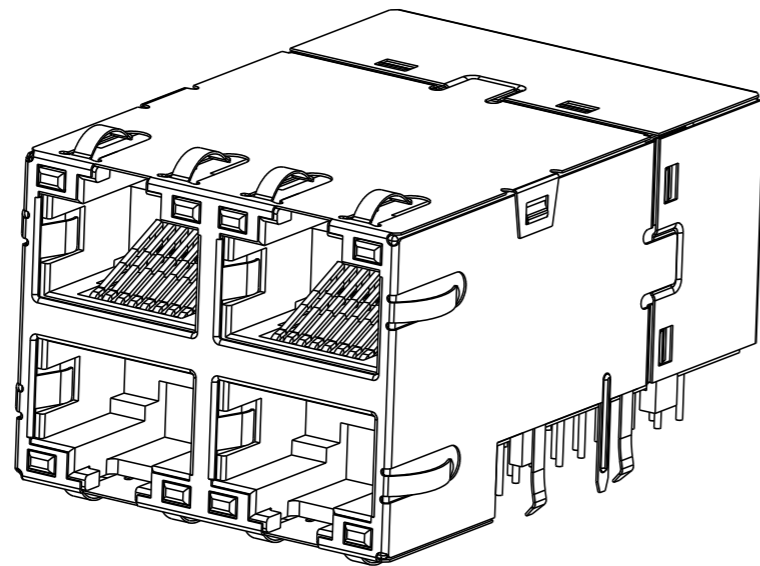


THIS DRAWING AND THE SUBJECT MATTER SHOWN THEREON ARE CONFIDENTIAL AND THE PROPERTY OF BEL/STEWART/TRP CONNECTOR AND SHALL NOT BE REPRODUCED, COPIED, OR USED IN ANY MANNER WITHOUT THE WRITTEN CONSENT OF TRP CONNECTOR.

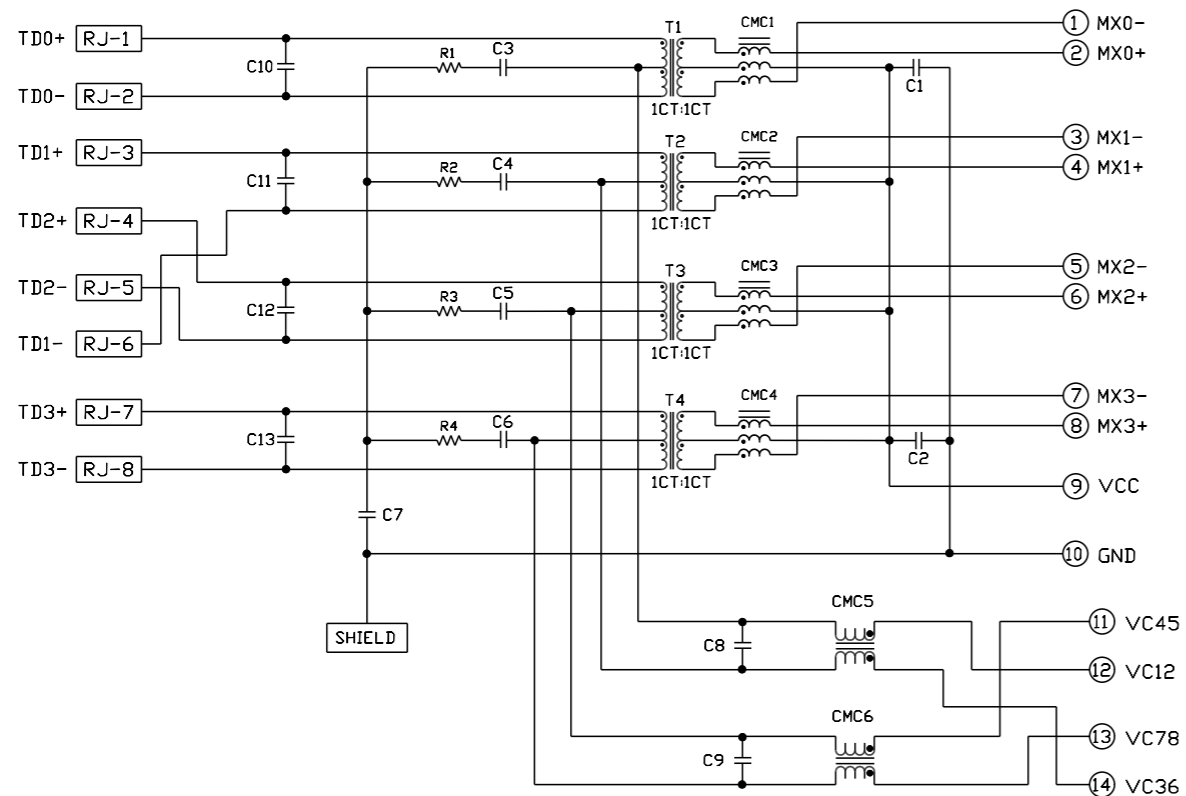
PRODUCT MAY BE PROTECTED BY ONE OR MORE OF THE FOLLOWING US PATENTS:
 5736910 5939955 6425781 6428361 6554638 6840817 7123117
 7429195 7717749 7808751 6217391 6149050 7924130

| REVISIONS | | | | |
|-----------|-----|-----------------|-----------|----------|
| # | LT# | DESCRIPTION | DATE | OWN APVD |
| 1 | | INITIAL RELEASE | 28DEC2020 | JY TY |
| 3 | | SEE LAST SHEET | 12OCT2021 | QL TY |



- 1 MATERIALS:
 - HOUSING: THERMOPLASTIC, BLACK, FLAMMABILITY RATING UL 94V-0.
 - SHIELD: STAINLESS STEEL, 0.15mm THK, PREPLATED WITH 0.76um MINIMUM SEMI-BRIGHT NICKEL.
 - CONTACTS: PHOSPHOR BRONZE, PLATED WITH 1.27um MIN OVERALL NICKEL UNDERPLATE, SELECTIVE 1.27um MIN GOLD AT MATING INTERFACE AND 2.54um MIN MATTE TIN ON SOLDER TAILS.
- 2 MAGNETICS
 APPLICATION: 2.5G BASE-T, 4PPOE
 IMPEDANCE: 100 OHMS
 TURNS RATIO (CHIP:CABLE): 1:1 ALL FOUR PAIRS
 OPEN CIRCUIT INDUCTANCE (OCL):
 ALL CHANNELS 180uH MIN @ 100KHz, 0.1 VRMS WITH 8mA DC BIAS FROM 0°C TO 70°C,
 120uH MIN @ 100KHz, 0.1 VRMS WITH 23mA DC BIAS FROM 0°C TO 70°C
 ALL FOUR PAIRS BI-DIRECTIONAL
 POE CURRENT: 1A DC MAX
 PERFORMANCE @ 25°C:
 RETURN LOSS (RL): 16dB MIN FROM 1MHz TO 40MHz,
 16-10 LOG10 (f/40)dB MIN FROM 40MHz TO 125MHz.
 ISOLATION VOLTAGE: 2250VDC(MAX) FOR 60 SECONDS WITH A RISE TIME OF 500V/SEC AND WITH ALL PORTS CONNECTED.

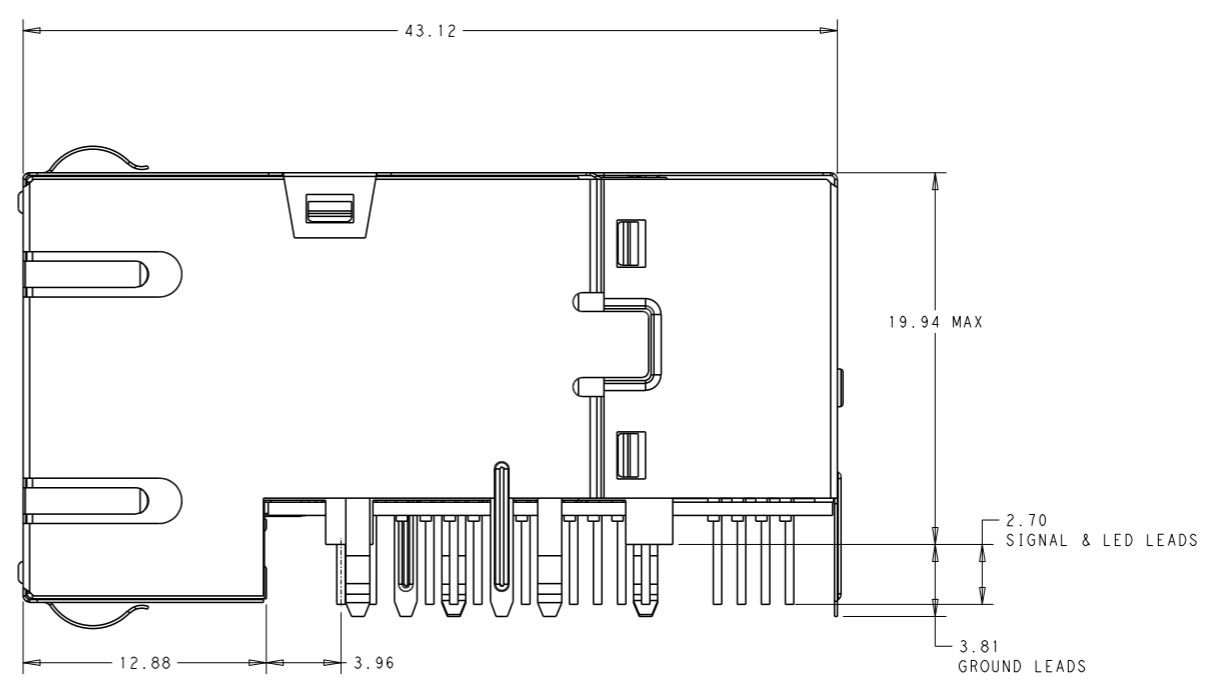
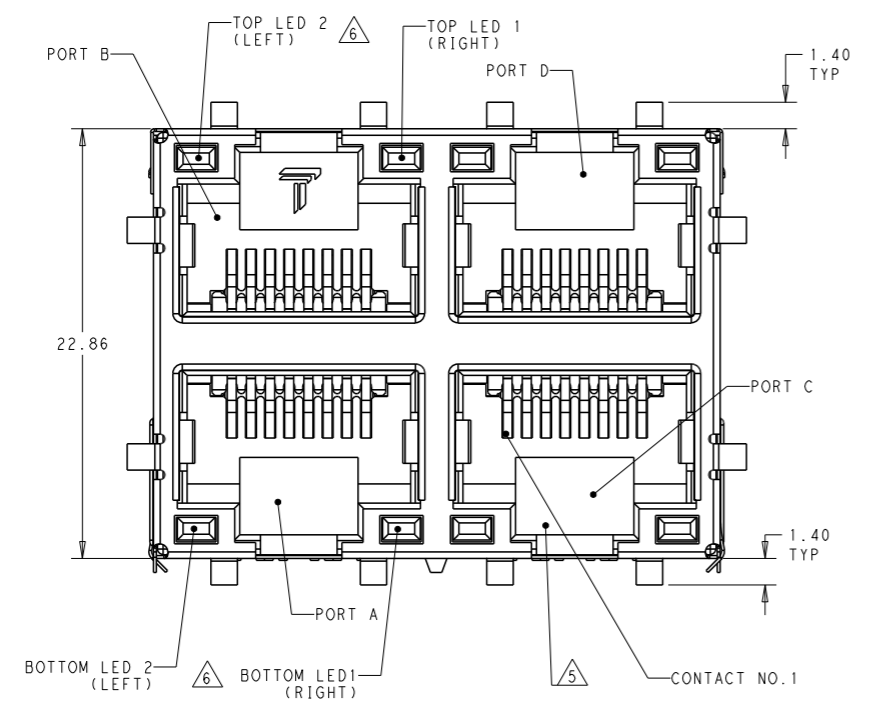
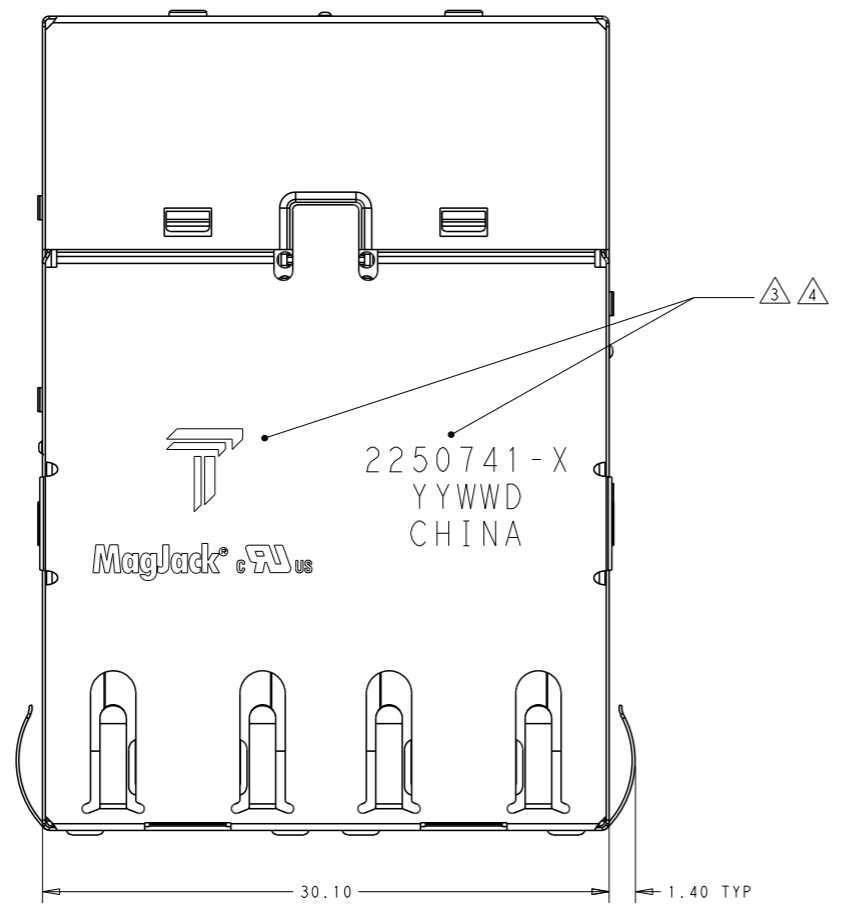
S9UQG02C1 2.5G 4PPOE CIRCUIT




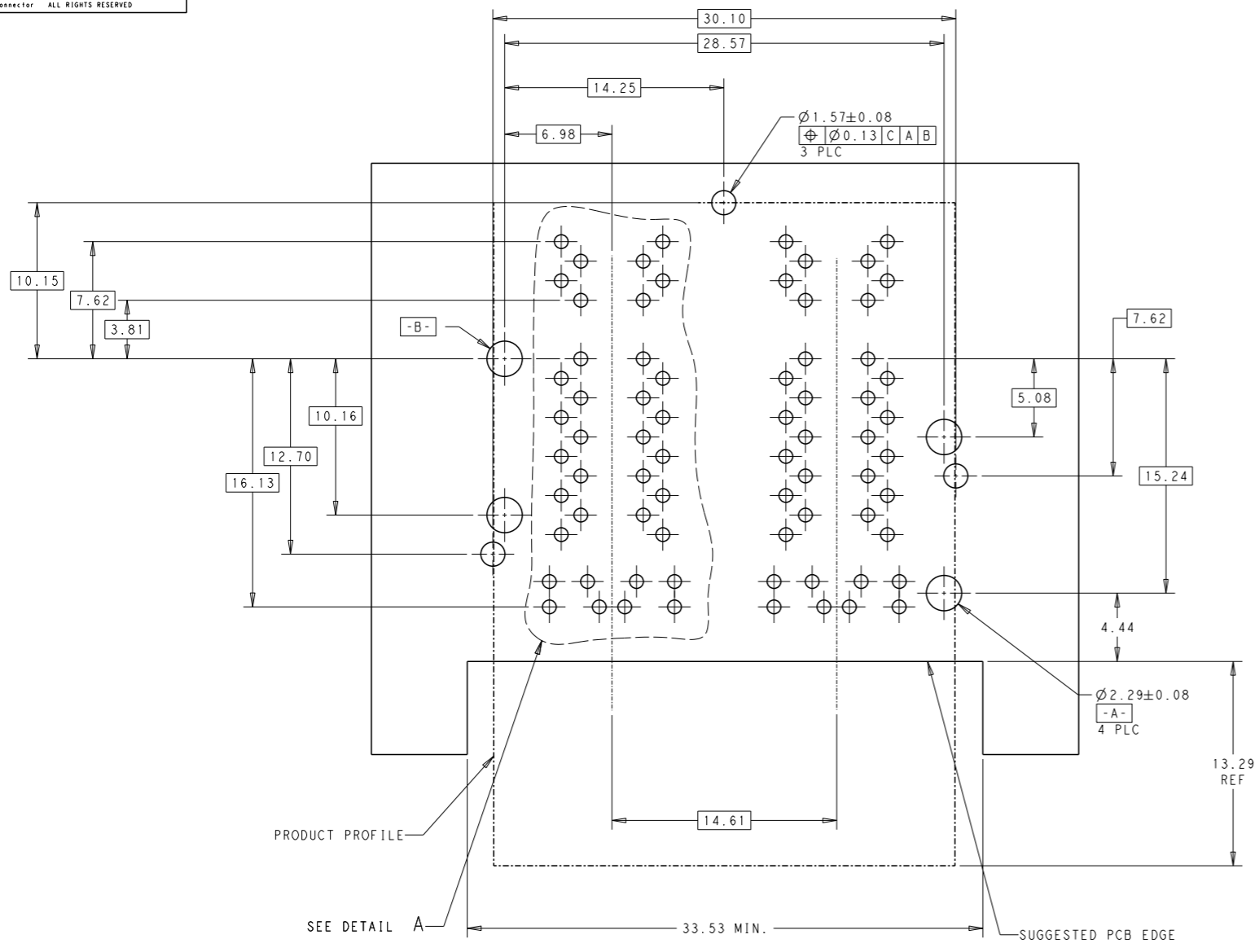
C1-C2 =10nF, 50V, X7R,CAPACITORS
 C3-C6 =22nF, 100V, CAPACITORS
 C7 =1000pF, 2kV, CAPACITOR
 C8-C9 =100nF, 100V, X7R, CAPACITORS
 C10-C13 =1pF, 25V, X7R, CAPACITORS
 R1-R4 =75 Ohms,1/16W,RESISTORS

- 3 PART NUMBER, DATE CODE AND COUNTRY OF ORIGIN ARE LOCATED IN THE APPROXIMATE AREA SHOWN. DATE CODE YY IS YEAR,WW IS WORK WEEK, D IS DAY OF WEEK, WITH SUNDAY=1
- 4 TRP LOGO AND AGENCY APPROVAL LOGO ARE LOCATED IN THE APPROXIMATE AREA SHOWN.
- 5 RJ45 CAVITIES CONFORM TO FCC RULES AND REGULATION PART 68 SUBPART F.
- 6 LEDS ARE DRIVEN WITH CONSTANT CURRENT AT APPROX. 20mA.
 DOMINANT WAVELENGTH (λD)
 GREEN, 568nm TYP AT IF=20mA
 YELLOW, 588nm TYP AT IF=20mA
 FORWARD VOLTAGE(VF)
 GREEN, 2.2V TYP AT IF=20mA
 YELLOW, 2.1V TYP AT IF=20mA
- 7 INDICATED MAGNETIC CONNECTIONS ARE SYMMETRICAL AND SUPPORT AUTO-MDI/MDIX.
- 8 OPERATING TEMPERATURE: FROM 0 °C TO 70 °C
- 9 THE PART IS RECOMMENDED FOR WAVE SOLDERING PROCESS, PEAK TEMPERATURE 260°C MAX, 10 SECONDS MAX.IT IS RECOMMENDED THAT THE CONNECTOR SHIELD AND BODY BE MASKED TO PROTECT IT FROM DIRECT EXPOSURE TO THE SOLDER WAVE TO AVOID DAMAGE TO THE CONNECTOR AND LEDS.

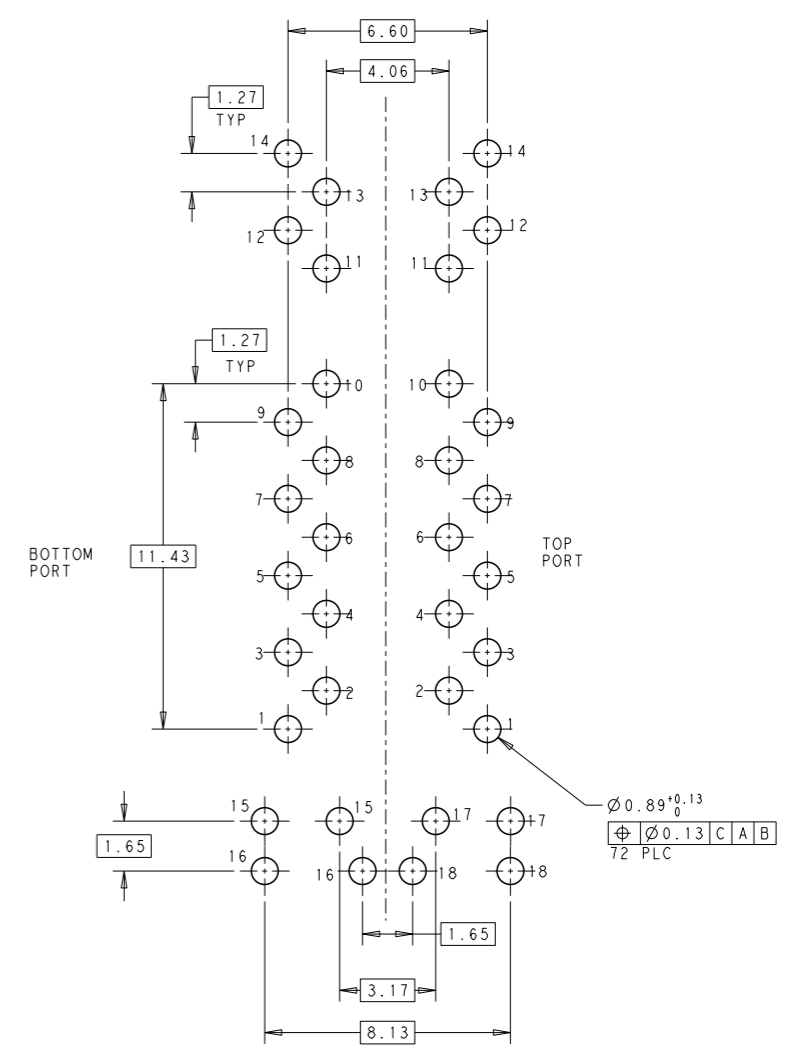
| GREEN/YELLOW BOTTOM LED 2(LEFT) | GREEN/YELLOW BOTTOM LED 1(RIGHT) | GREEN/YELLOW TOP LED 2(LEFT) | GREEN/YELLOW TOP LED 1(RIGHT) | 2250741-1 PART NUMBER |
|--|-------------------------------------|--|----------------------------------|--------------------------|
| THIS DRAWING IS A CONTROLLED DOCUMENT. AND COMPLIANT TO ASME Y14.5M | | | | |
| DIMENSIONS: mm | | TOLERANCES UNLESS OTHERWISE SPECIFIED: 0 PLC ±0.25 1 PLC ±0.25 2 PLC ±0.25 3 PLC ± 4 PLC ± ANGLES ±° | | |
| PRODUCT SPEC 108-104004 | | APPLICATION SPEC - | | |
| DWN: JARRY YE/TERRY LIU CHK: TOWER YU/BILL LIU APVD: TONY YUAN | | 28DEC2020 28DEC2020 28DEC2020 | | |
| MODEL NAME MAGJACK STACK 4PPOE | | DESC 2X2 S9UQG02C1 2.5G 4PPOE 100W CIRCUIT, OFFSET, WITH LED | | |
| SIZE A1 | | CAGE CODE DRAWING NO 2250741 | | |
| CUSTOMER DRAWING | | SCALE NTS | | |
| SHEET 1 | | OF 5 | | REV 3 |



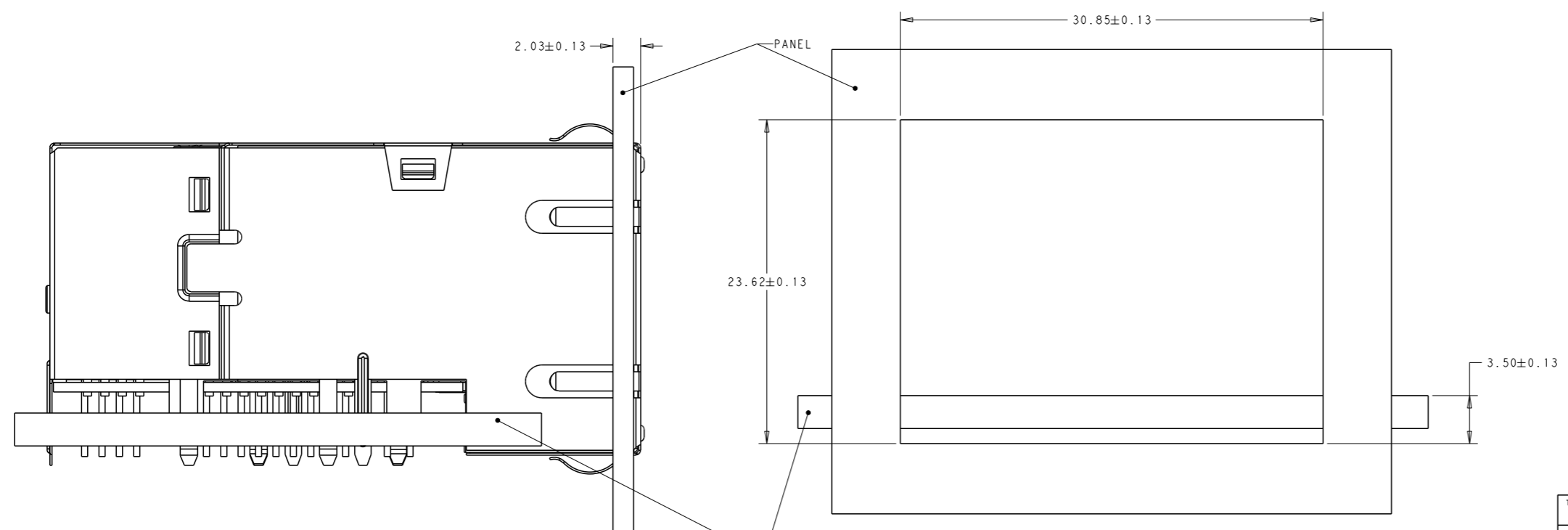
| | | | |
|---|--|--|--|
| THIS DRAWING IS A CONTROLLED DOCUMENT. AND COMPLIANT TO ASME Y14.5M | | DWN: JARRY YE/TERRY LIU 28DEC2020 CHK: TOWER YU/BILL LIU 28DEC2020 APP'D: TONY YUAN 28DEC2020 |  trp CONNECTOR DONGGUAN CHINA |
| DIMENSIONS: mm | | MODEL NAME: MAGJACK STACK 4PPOE SIZE: A1 CAGE CODE: C=2250741 DRAWING NO: 108-104004 DRAWING NO: 2250741 SCALE: NTS SHEET: 2 OF 5 REV: 3 | |
| TOLERANCES UNLESS OTHERWISE SPECIFIED: 0 PLC ±0.25 1 PLC ±0.25 2 PLC ±0.25 3 PLC ± 4 PLC ± ANGLES ± | | DESC: 2X2 S9U0G02C1 2.5G 4PPOE 100W CIRCUIT, OFFSET, WITH LED | RESTRICTED TO: - |
| PRODUCT SPEC: 108-104004 | | APPLICATION SPEC: - | |



RECOMMENDED PC BOARD LAYOUT VIEW FROM COMPONENT SIDE

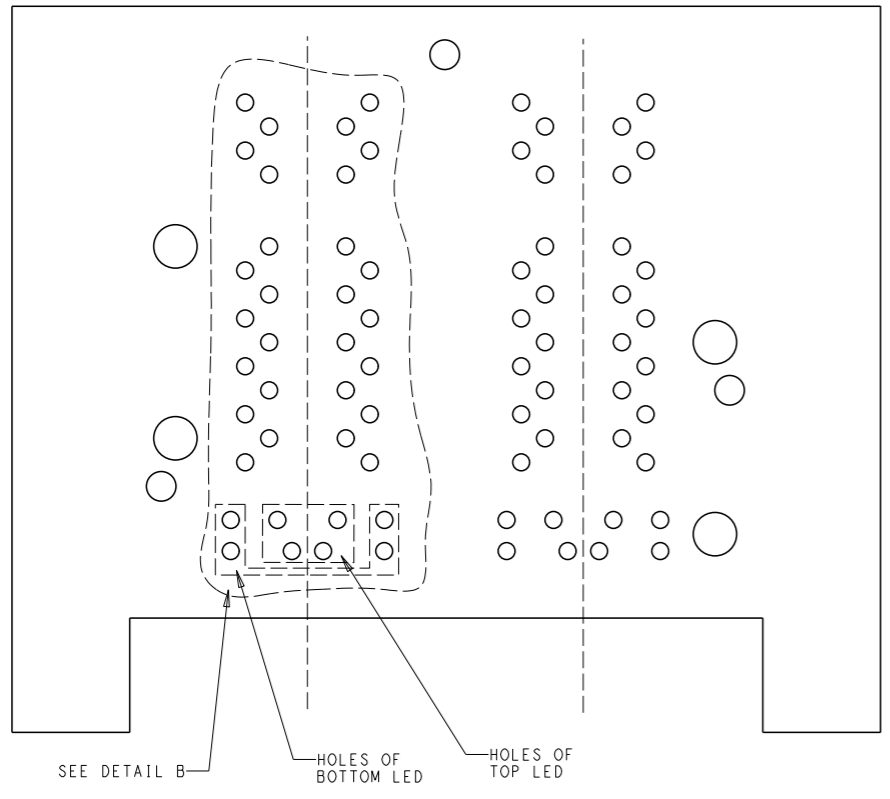


DETAIL A SCALE 8:1

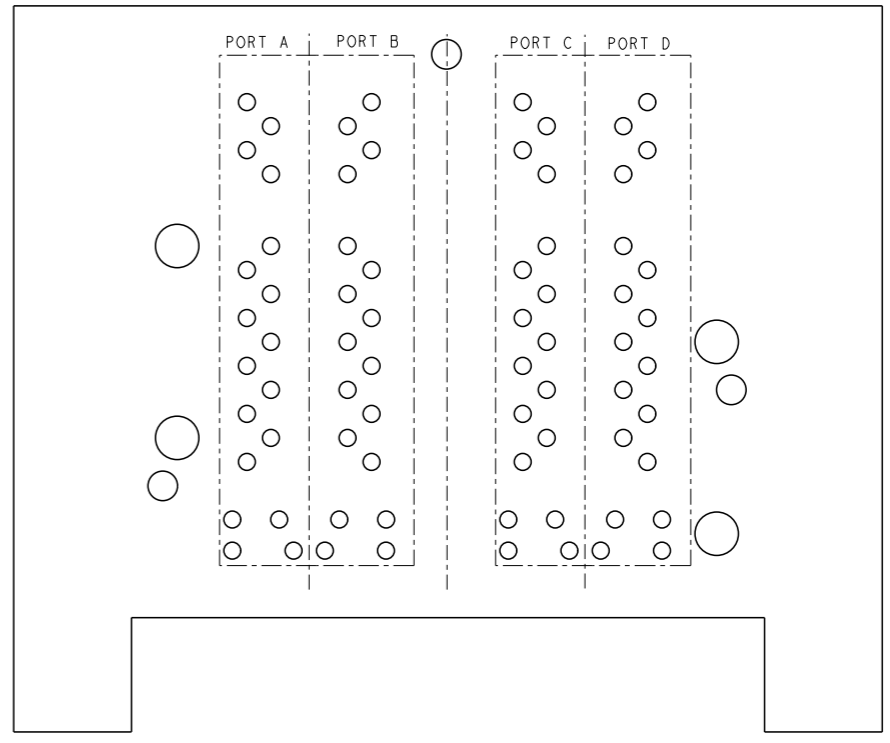
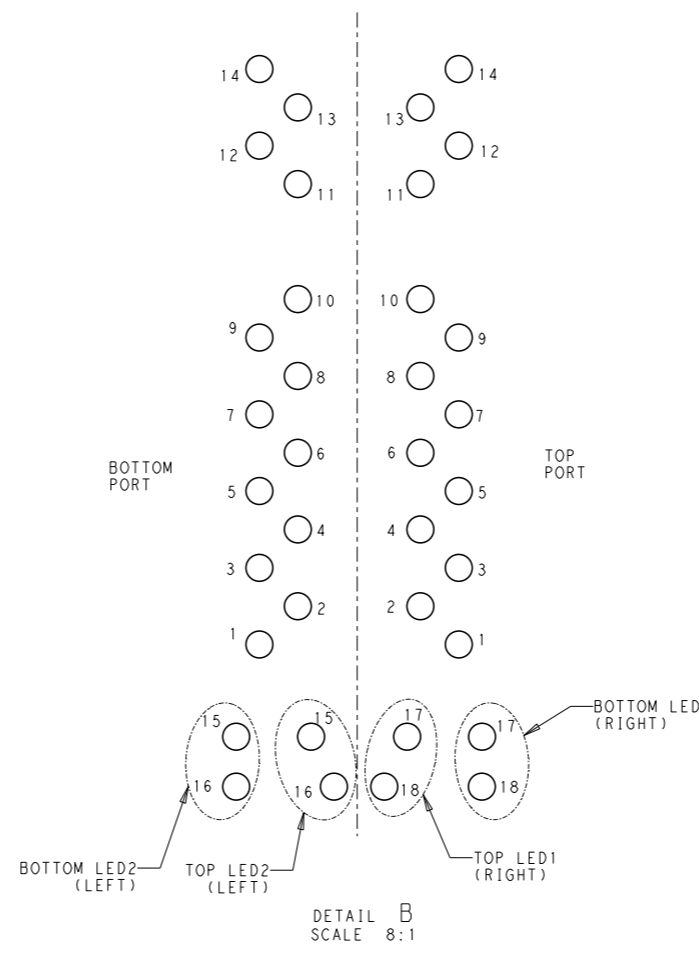


RECOMMENDED PANEL CUT-OUT

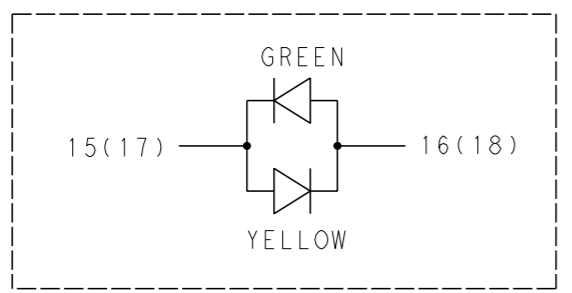
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|---|--|--|--|---|
| THIS DRAWING IS A CONTROLLED DOCUMENT. AND COMPLIANT TO ASME Y14.5M | | DWN: JARRY YE/TERRY LIU 28DEC2020 CHK: TOWER YU/BILL LIU 28DEC2020 APP'D: TONY YUAN 28DEC2020 | | trp CONNECTOR DONGGUAN CHINA |
| DIMENSIONS: mm | | TOLERANCES UNLESS OTHERWISE SPECIFIED: 0 PLC ±0.25 1 PLC ±0.25 2 PLC ±0.25 3 PLC ±0.25 4 PLC ±0.25 ANGLES ±° | | |
| PRODUCT SPEC: 108-104004 | | APPLICATION SPEC: - | | SIZE: A1 CAGE CODE: - DRAWING NO: 2250741 RESTRICTED TO: - |
| CUSTOMER DRAWING | | | | SCALE: NTS SHEET 3 OF 5 REV 3 |





LED HOLE DESIGNATIONS VIEWED FROM COMPONENT SIDE




PORT ASSIGNMENT
 COMPONENT SIDE VIEW



| | | | | | |
|---|--|---|--|--|--|
| THIS DRAWING IS A CONTROLLED DOCUMENT. AND COMPLIANT TO ASME Y14.5M | | DWN: JARRY YE/TERRY LIU 28DEC2020 CHK: TOWER YU/BILL LIU 28DEC2020 APP'D: TONY YUAN 28DEC2020 | |  trp CONNECTOR DONGGUAN CHINA | |
| DIMENSIONS: mm | | TOLERANCES UNLESS OTHERWISE SPECIFIED: | | MODEL NAME: MAGJACK STACK 4PPOE | |
|  0 PLC ±0.25 1 PLC ±0.25 2 PLC ±0.25 3 PLC ± 4 PLC ± ANGLES ± | | APPLICATION SPEC: - | | DESC: 2X2 S9U0G02C1 2.5G 4PPOE 100W CIRCUIT, OFFSET, WITH LED | |
| PRODUCT SPEC: 108-104004 | | SCALE: NTS | | SIZE: A1 CAGE CODE: 2250741 DRAWING NO: CUSTOMER DRAWING | |
| | | | | SHEET 4 OF 5 REV 3 | |

| 2250741 | | | | | STOCK DISPOSITION | | | |
|---|-----|--|-----------------------|----------|----------------------------|---------------------------------|-------------------------------|--------------------------------|
| 2X2 S9UOG02C1 2.5G 4PPOE 100W CIRCUIT, OFFSET, WITH LED | | | | | A: CONTINUE AS IS | | D: USE OLD FIRST | |
| | | | | | B: REWORK | | E: PARTS TO CONFORM | |
| | | | | | C: SCRAP | | F: OTHER | |
| REVISION HISTORY | | | | | | | | |
| DATE | REV | CHANGES | REASON | APPROVAL | INVENTORY (A,B,C,D,E,F) | WORK IN PROCESS (A,B,C,F) | SUPPLIER ORDERS (A,E,F) | OPEN CUST ORDERS (A,E,F) |
| 28DEC2020 | 1 | - | INITIAL RELEASE | TY | - | - | - | - |
| 09MAR2021 | 2 | SHEET 3 A6-B3 AREA: 2.92 CHANGE TO 2.03±0.13, 30.85 CHAGNE TO 30.85±0.13, 23.62 CHANGE TO 23.62±0.13, 3.55 CHANGE TO 3.50±0.13 SHEET 2 A4 AREA: 12.83 CHANGE TO 12.88 4.06 CHANGE TO 3.96 | MEET CUSTOMER REQUEST | TY | - | - | - | - |
| 12OCT2021 | 3 | CIRCUIT CHANGE | CUSTOMER REQUEST | TY | - | - | - | - |

| | | | | | |
|--|--|--|--|--|--|
| THIS DRAWING IS A CONTROLLED DOCUMENT. AND COMPLIANT TO ASME Y14.5M | | DWN: JARRY YE/TERRY LIU 28DEC2020 CHK: TOWER YU/BILL LIU 28DEC2020 APP'D: TONY YUAN 28DEC2020 | |  trp CONNECTOR DONGGUAN CHINA | |
| DIMENSIONS: mm | | TOLERANCES UNLESS OTHERWISE SPECIFIED: 0 PLC ±0.25 1 PLC ±0.25 2 PLC ±0.25 3 PLC ± 4 PLC ± ANGLES ±° | | MODEL NAME: MAGJACK STACK 4PPOE | |
| PRODUCT SPEC: 108-104004 | | APPLICATION SPEC: - | | DESC: 2X2 S9UOG02C1 2.5G 4PPOE 100W CIRCUIT, OFFSET, WITH LED | |
| | | SIZE: A1 | | CAGE CODE: C=2250741 | |
| | | CUSTOMER DRAWING | | SCALE: NTS SHEET 5 OF 5 REV 3 | |